

# COM Express™ PCOM-B705GT User's Guide R1.1

## Revision History

Rev.	Note	Date
R1.0	Release	2023/4/10
R1.1	Revise Chapter 9 Portwell Software Tool	2024/1/15

## Contents

1	Introduction .....	9
1.1	PCOM-B705GT SKU List .....	10
2	Block Diagram .....	11
3	Specifications .....	12
3.1	Supported Operating Systems.....	14
3.2	Windows OS Driver .....	15
3.3	Electrical Characteristics .....	16
3.4	Power Consumption .....	20
3.5	Mechanical Dimensions.....	21
3.6	4x SO-DIMM Slot Design.....	23
3.7	Environmental Specifications.....	24
3.8	Ordering Guide .....	25
4	Thermal Solution.....	26
5	Pin out Tables .....	29
6	BIOS Setup Items .....	34
6.1	Entering Setup -- Launch System Setup .....	35
6.2	Main.....	36
6.3	Configuration .....	37
	CPU Configuration .....	38
	Chipset Configuration .....	40
	LAN Configuration .....	41
	PCI/PCIE Configuration.....	42
	SATA Configuration .....	45
	USB Configuration.....	46
	Power Control Configuration .....	48

TPM Configuration .....	49
Super IO Configuration.....	51
H/W Monitor .....	54
Serial Port Console Redirection .....	55
EC Firmware Update .....	58
6.4 Security.....	59
6.5 Boot.....	61
6.6 Save & Exit.....	62
7 System Resources.....	63
8 BIOS/EC Update.....	64
9 PORTWELL Software Tool.....	69
10 Packaging Information .....	70
11 Industry Specifications .....	71

## List of Tables

Table 1 PCOM-B705GT SKU .....	10
Table 2 OS Support List .....	14
Table 3 Electrical Characteristics .....	16
Table 4 PCOM-B705GT Power Consumption .....	20
Table 5 Environmental Specifications.....	24
Table 6 Ordering Guide - PCOM-B705GT.....	25
Table 7 Ordering Guide - Accessory.....	25
Table 8 Pin out Description.....	33
Table 9 System Resources.....	63
Table 10 Packaging Information .....	70

## List of Figures

Figure 1 Block Diagram.....	11
Figure 2 ATX Style Boot .....	17
Figure 3 AT Style Boot.....	18
Figure 4 PWR_OK Signal Brief Diagram.....	19
Figure 5 Mechanical Dimension - Top .....	21
Figure 6 Mechanical Dimension - Bottom.....	22
Figure 7 SO-DIMM Slot Design.....	23
Figure 8 Cooler Mechanical Dimension.....	26
Figure 9 Heatsink Mechanical Dimension .....	27
Figure 10 Heatspreader Mechanical Dimension .....	28

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# 1 Introduction

PCOM-B705GT, a Type 7 COM Express® basic size(125 x 95 mm) module which based on Intel® Xeon® D-1700 series processors. In this architecture, it could provide up to 10 cores / 20 threads processors with the maximum TDP=67w, and 4x 10G KR ports. It also supports 16x PCIe 4.0, 16x PCIe 3.0 lanes, 4x USB 3.2 Gen1, and 2x SATA III ports. Selected SKUs support wide-temperature range.

PCOM-B705GT offer an effective upgrade path for solutions already using the previous D-1600 COM Express modules, ideal for applications in edge/micro server and alike, requiring relatively lower power consumption while supporting high computing performance and communication throughput.

## 1.1 PCOM-B705GT SKU List

Series	PCOM-B705GT				
Ordering P/N	AB1-3P64	AB1-3P66	AB1-3P65	AB1-3P67	AB1-3P68
<b>CPU Specifications</b>					
Processor	D-1746TER	D-1732TE	D-1735TR	D-1715TER	D-1712TR
# of Cores	10	8	8	4	4
# of Threads	20	16	16	8	8
Cache	15 MB	15 MB	15 MB	10 MB	10 MB
Base Frequency	2.0 GHz	1.9 GHz	2.2 GHz	2.4 GHz	2.0 GHz
Turbo Frequency	2.5 GHz	2.4 GHz	2.7 GHz	2.9 GHz	2.5 GHz
TDP	67 W	52 W	59 W	50 W	40 W
<b>Memory Specifications</b>					
Capacity	4x SO-DIMM	4x SO-DIMM	4x SO-DIMM	3x SO-DIMM	3x SO-DIMM
Speed	2666 MT/s	2666 MT/s	2666 MT/s	2666 MT/s	2666 MT/s
ECC	Yes	Yes	Yes	Yes	Yes
<b>I/O Specifications</b>					
PCIe	16x PCIe 4.0 16x PCIe 3.0	16x PCIe 4.0 16x PCIe 3.0	16x PCIe 4.0 16x PCIe 3.0	16x PCIe 4.0 16x PCIe 3.0	16x PCIe 4.0 16x PCIe 3.0
USB 3.0/2.0	4x	4x	4x	4x	4x
SATA	2x	2x	2x	2x	2x
Ethernet	4x 10G KR 1x GbE	4x 10G KR 1x GbE	4x 10G KR 1x GbE	4x 10G KR 1x GbE	4x 10G KR 1x GbE

Table 1 PCOM-B705GT SKUs

## 2 Block Diagram

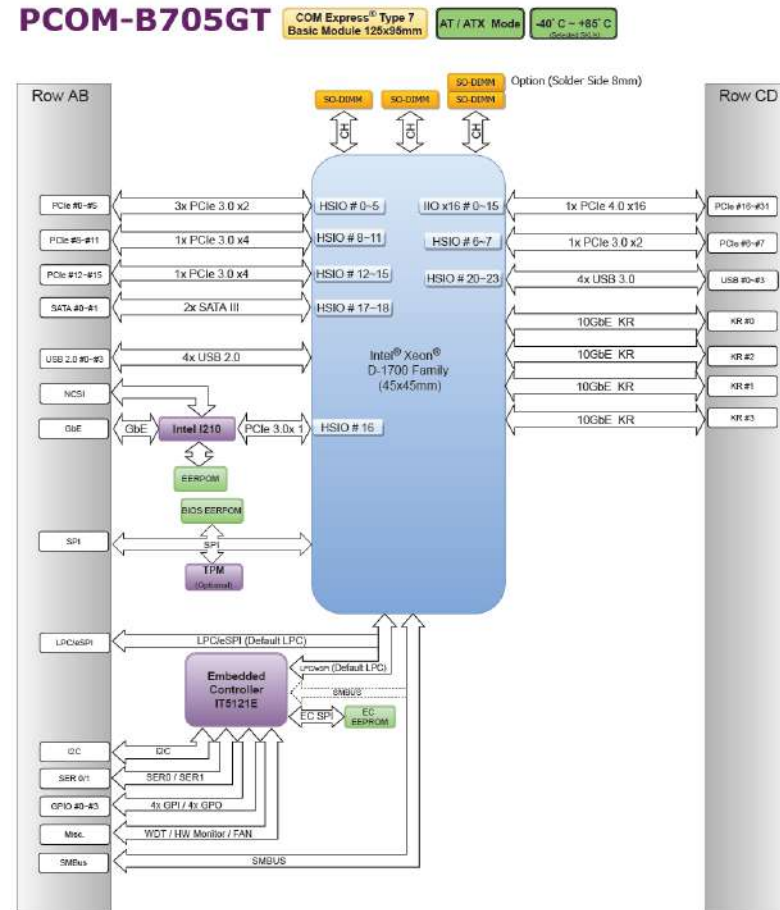


Figure 1 Block Diagram

### 3 Specifications

Product	➤ PCOM-B705GT																																										
Form Factor	➤ COM Express® Type 7, Basic Size (125mm X 95mm)																																										
Processor	<ul style="list-style-type: none"> <li>➤ Intel® Xeon® D-1746TER</li> <li>➤ Intel® Xeon® D-1735TR</li> <li>➤ Intel® Xeon® D-1732TE</li> <li>➤ Intel® Xeon® D-1715TER</li> <li>➤ Intel® Xeon® D-1712TR</li> </ul>																																										
BIOS	➤ AMI UEFI BIOS																																										
Memory	<ul style="list-style-type: none"> <li>➤ Support ECC and Non-ECC, up to 3 Channel Memory Operation</li> <li>➤ 4x DDR4 SO-DIMM slots with 3 channels for 8 Core / 10 Core SKUs, up to 128GB, 2666MT/s</li> <li>➤ 3x DDR4 SO-DIMM slots with 2 channels for 4 Core SKUs, up to 96GB, 2666MT/s</li> </ul>																																										
Ethernet	<ul style="list-style-type: none"> <li>➤ 1x GbE (via Intel® i210AT/IT)</li> <li>➤ 4x 10GbE: Mode Configuration, need different BIOS firmware for proper operation. <ul style="list-style-type: none"> <li>■ 100G and 50G SKU: provide default BIOS support 4x 10G support LEK 7.0 CEI Mode.</li> <li>■ For KR Mode Support, please contact sales for firmware support.</li> <li>■ For 4x 25GbE Support on 100G SKU, please contact sales for ODM request.</li> </ul> </li> </ul> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>SKU</th> <th>P/N</th> <th>CPU Spec</th> <th>Integrated Ethernet Bandwidth</th> <th>LEK 7.0 (CEI)</th> <th>LEK 7.6 (KR)</th> <th>SODIMM Qty</th> </tr> </thead> <tbody> <tr> <td>D-1746TER</td> <td>AB1-3P64Z</td> <td>10C, eTemp</td> <td>100G</td> <td>10G*4</td> <td>10G*4</td> <td>4</td> </tr> <tr> <td>D-1735TR</td> <td>AB1-3P65Z</td> <td>8C, cTemp</td> <td>50G</td> <td>10G*4</td> <td>10G*4</td> <td>4</td> </tr> <tr> <td>D-1732TE</td> <td>AB1-3P66Z</td> <td>8C eTemp</td> <td>50G</td> <td>10G*4</td> <td>10G*4</td> <td>4</td> </tr> <tr> <td>D-1715TER</td> <td>AB1-3P67Z</td> <td>4C, eTemp</td> <td>50G</td> <td>10G*4</td> <td>10G*4</td> <td>3</td> </tr> <tr> <td>D-1712TR</td> <td>AB1-3P68Z</td> <td>4C, cTemp</td> <td>50G</td> <td>10G*4</td> <td>10G*4</td> <td>3</td> </tr> </tbody> </table>	SKU	P/N	CPU Spec	Integrated Ethernet Bandwidth	LEK 7.0 (CEI)	LEK 7.6 (KR)	SODIMM Qty	D-1746TER	AB1-3P64Z	10C, eTemp	100G	10G*4	10G*4	4	D-1735TR	AB1-3P65Z	8C, cTemp	50G	10G*4	10G*4	4	D-1732TE	AB1-3P66Z	8C eTemp	50G	10G*4	10G*4	4	D-1715TER	AB1-3P67Z	4C, eTemp	50G	10G*4	10G*4	3	D-1712TR	AB1-3P68Z	4C, cTemp	50G	10G*4	10G*4	3
SKU	P/N	CPU Spec	Integrated Ethernet Bandwidth	LEK 7.0 (CEI)	LEK 7.6 (KR)	SODIMM Qty																																					
D-1746TER	AB1-3P64Z	10C, eTemp	100G	10G*4	10G*4	4																																					
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D-1732TE	AB1-3P66Z	8C eTemp	50G	10G*4	10G*4	4																																					
D-1715TER	AB1-3P67Z	4C, eTemp	50G	10G*4	10G*4	3																																					
D-1712TR	AB1-3P68Z	4C, cTemp	50G	10G*4	10G*4	3																																					

Graphic & Audio	➤ No Support
PCI Express	➤ 16x PCIe 4.0 and 16x PCIe 3.0
I/O	<ul style="list-style-type: none"> <li>➤ 4x USB 3.0 / 2.0</li> <li>➤ 2x SATA</li> <li>➤ 8 bit GPIO (default 4 in / 4 out)</li> <li>➤ I2C / SMBus</li> <li>➤ 2x UART</li> </ul>
Hardware Monitors	➤ ITE series Embedded Controller, Voltage, Fan and Temperature
Security	➤ TPM 2.0
Power Management	➤ ACPI 4.0
Environment	<ul style="list-style-type: none"> <li>➤ Operating Temperature: 0°C ~+60°C Extended: -40°C ~+80°C (selected SKU)</li> <li>➤ Storage Temperature: -40°C ~+85°C</li> <li>➤ Relative Humidity 5%~95%</li> </ul>

Table 2 PCOM-B705GT SPEC

## 3.1 Supported Operating Systems

The PCOM-B705GT supports the following operating systems.

Vendor	Operating System	Support
Windows	Microsoft Windows 10 IoT Enterprise LTSC	Intel, Microsoft
	Microsoft Windows Server 19H1, 19H2, 20H1	Intel, Microsoft
Linux	Red Hat Enterprise Linux 7.6 or latest	Red Hat
	SUSE Linux Enterpriser Server 12 SP4 or latest	SUSE, Open Source
	SUSE Linux Enterpriser Server 15 SP2 or latest	SUSE, Open Source
	Ubuntu 19.04 or latest	Canonical, Open Source
	Wind River Linux	Wind River
	Yocto Project BSP tool-based embedded Linux(64-bit)	Intel, Open Source
RTOS	Wind River VxWorks	Wind River
VMM	Linux KVM	Open Source
	ACRN	Open Source
	VMWare ESXi	VMware, Open Source
	Microsoft Windows Hyper-V: Windows Server 19H1	Microsoft
	Microsoft Windows Hyper-V: Windows Server 19H2	
	Microsoft Windows Hyper-V: Windows Server 20H1	
	Microsoft Azure	Microsoft

Table 2 OS Support list

Portwell does not endorse/validate/support any specific Linux distribution or entity mentioned on this list.

Portwell recommends customers to work with Linux vendors/open-source communities to find feature list and support model.

## 3.2 Windows OS Driver

Please download the drivers from Portwell download center website [http://www.portwell.tw/support/download\\_center.php](http://www.portwell.tw/support/download_center.php)

### 3.3 Electrical Characteristics

Input voltage	+12V $\pm$ 5%
RTC Battery	From Carrier
Power on mode	ATX Mode & AT Mode

Table 3 Electrical characteristics



● ATX Power Sequence

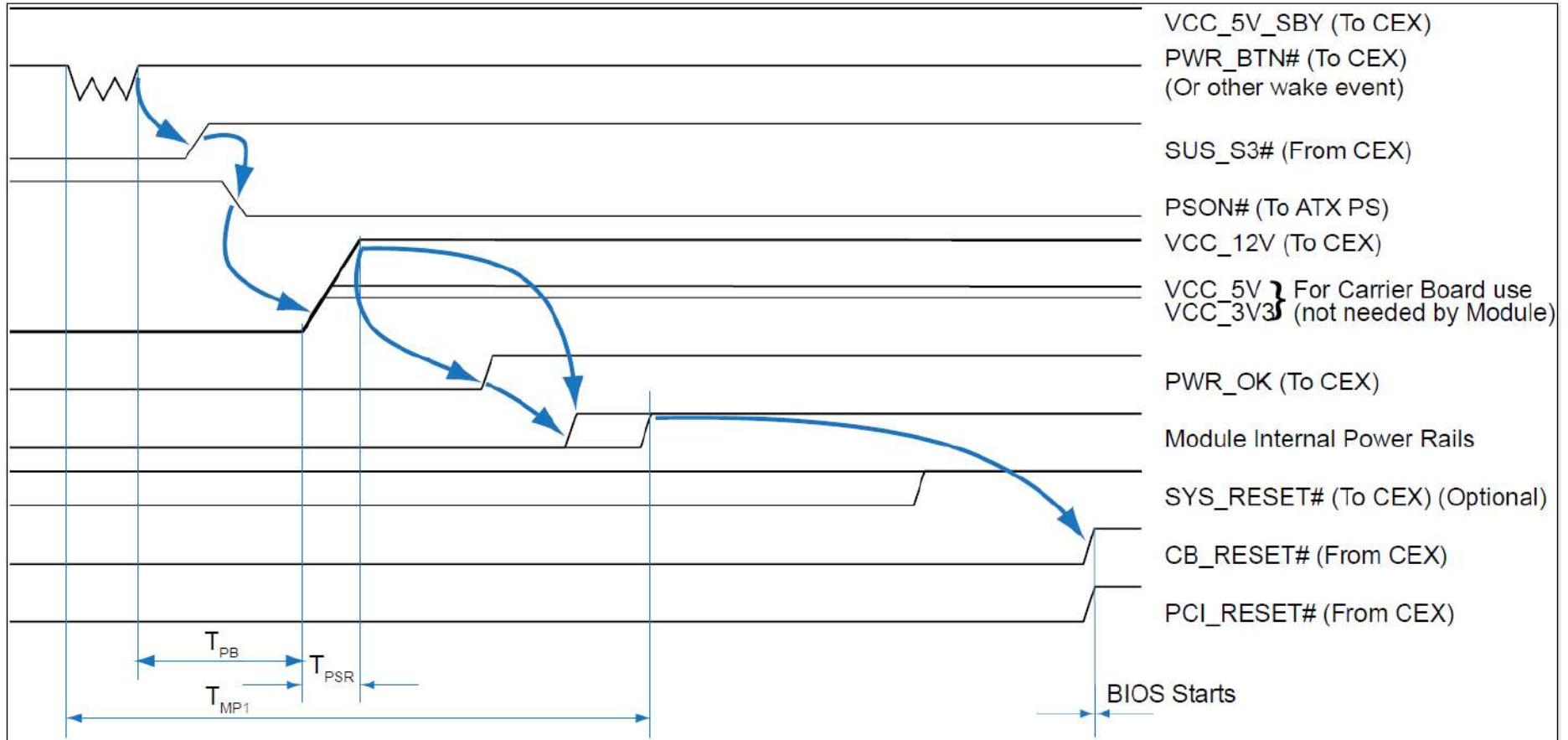


Figure 2 ATX style boot  
 (Reference from COM Express® Carrier Design Guide)

● AT Power Sequence

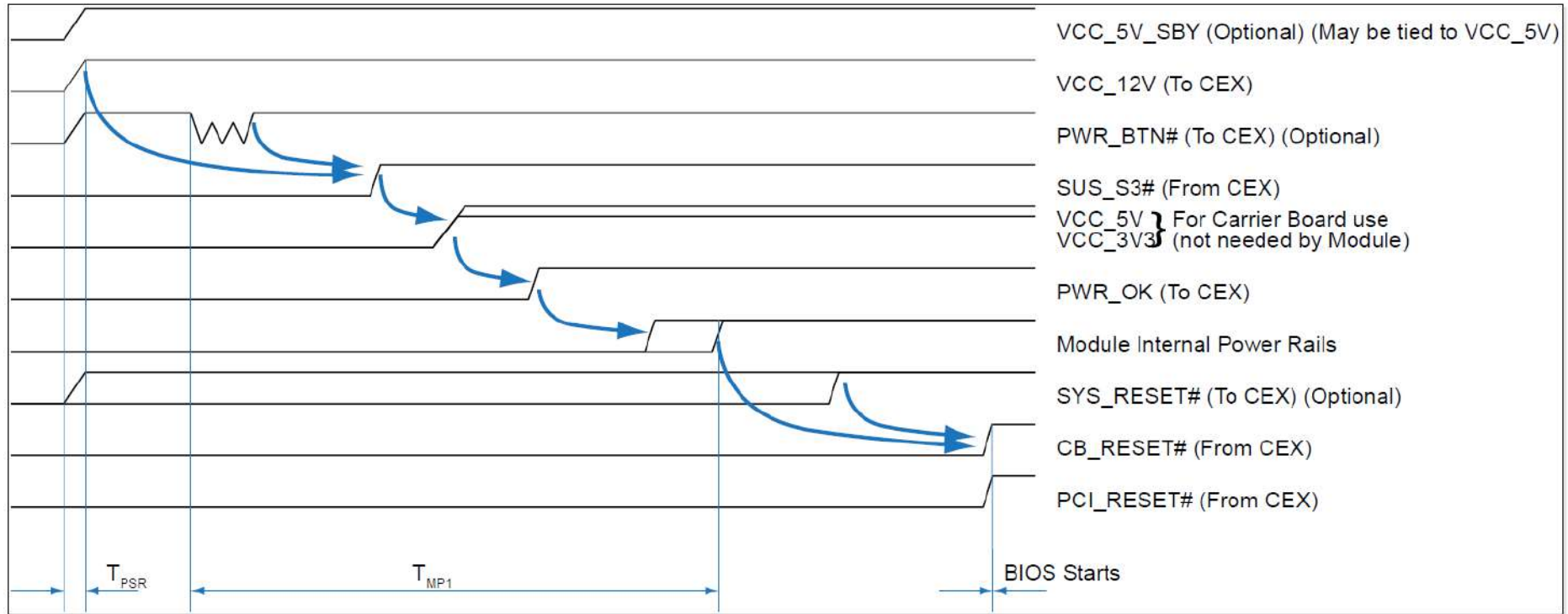


Figure 3 AT style boot  
 (Reference from COM Express® Carrier Design Guide)

- PWR\_OK signal

Carrier board hardware must keep this signal low until all power rails and clocks are stable. Releasing PWR\_OK too early or not driving it low at all may cause boot up abnormal symptom. It is a good design implementation to delay the PWR\_OK signal a little (at least 100ms) after all carrier board power rails are up, to ensure a stable system.

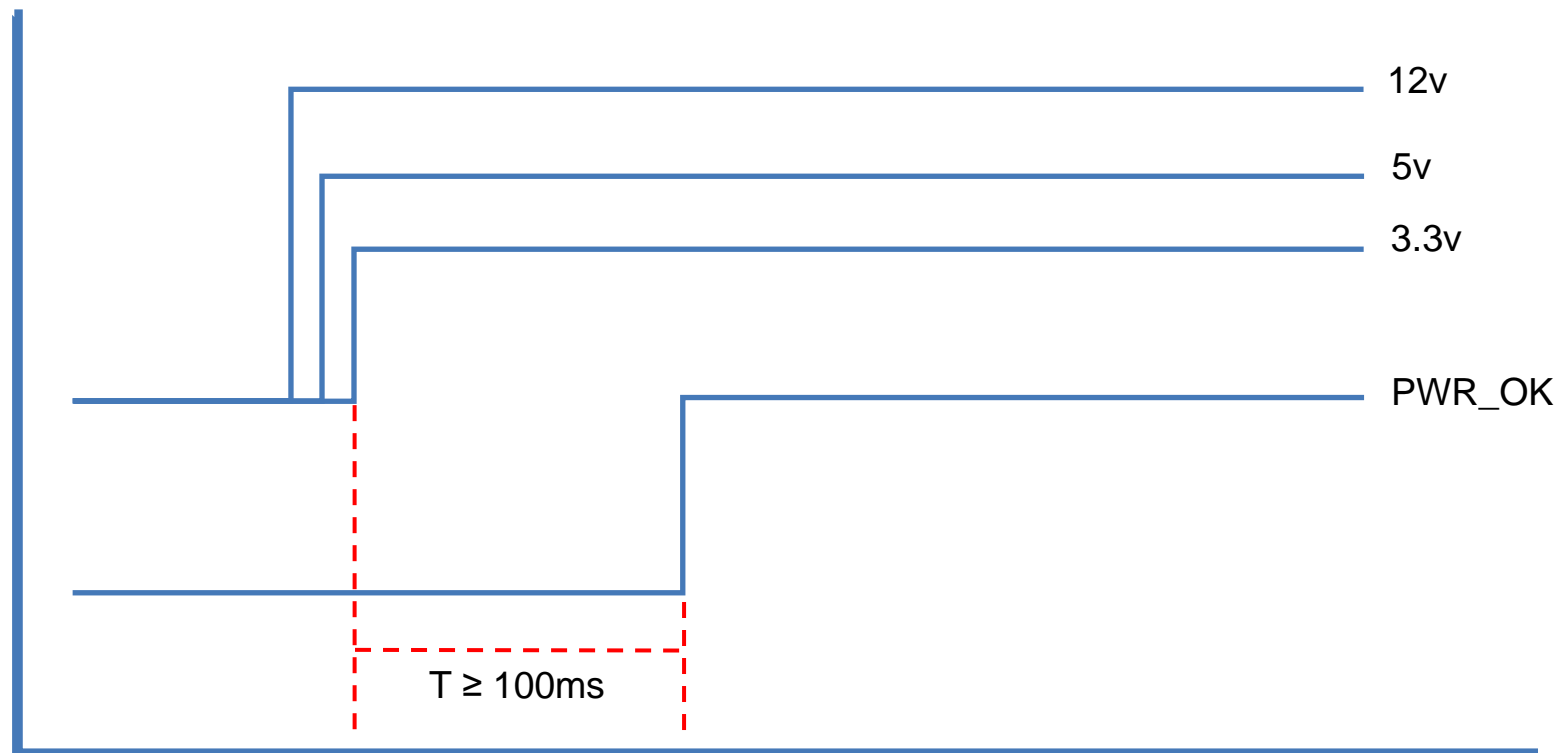


Figure 4 PWR\_OK signal brief diagram

## 3.4 Power Consumption

Series	PCOM-B705GT				
Ordering P/N	AB1-3P64	AB1-3P66	AB1-3P65	AB1-3P67	AB1-3P68
Processor	D-1746TER	D-1732TE	D-1735TR	D-1715TER	D-1712TR
<b><u>Power Consumption</u></b>					
S0 Idle	1.56A	1.54A	1.59A	1.64A	1.56A
100% workload without turbo mode	4.48A	4.13A	3.84A	3.43A	2.99A
100% Workload with turbo mode	4.83A	4.35A	3.85A	3.81A	3.10A
Peak Current	2.83A	2.69A	2.78A	2.48A	2.36A

Table 4 PCOM-B705GT Power Consumption

### 3.5 Mechanical Dimensions

- Top Side Dimension

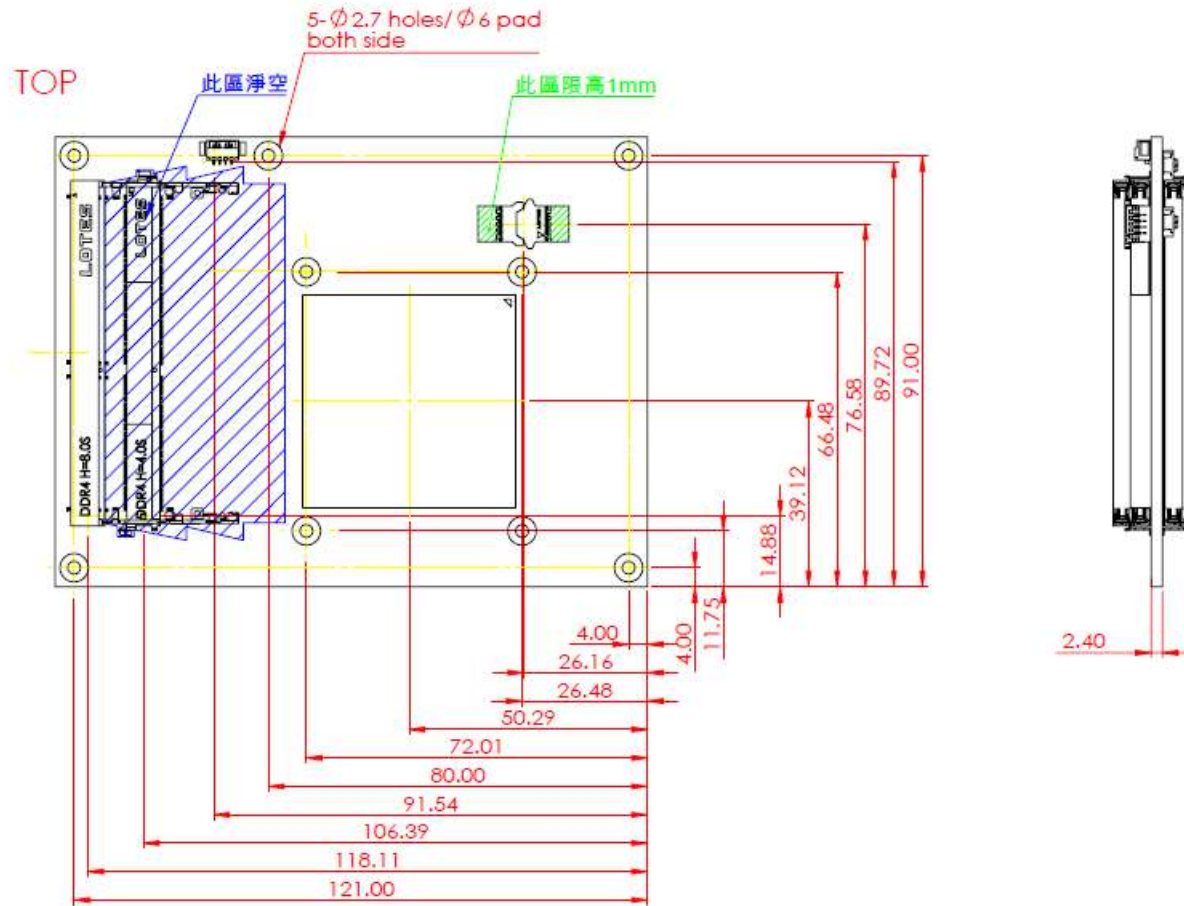


Figure 5 Mechanical Dimension - Top

- Bottom Side Dimension

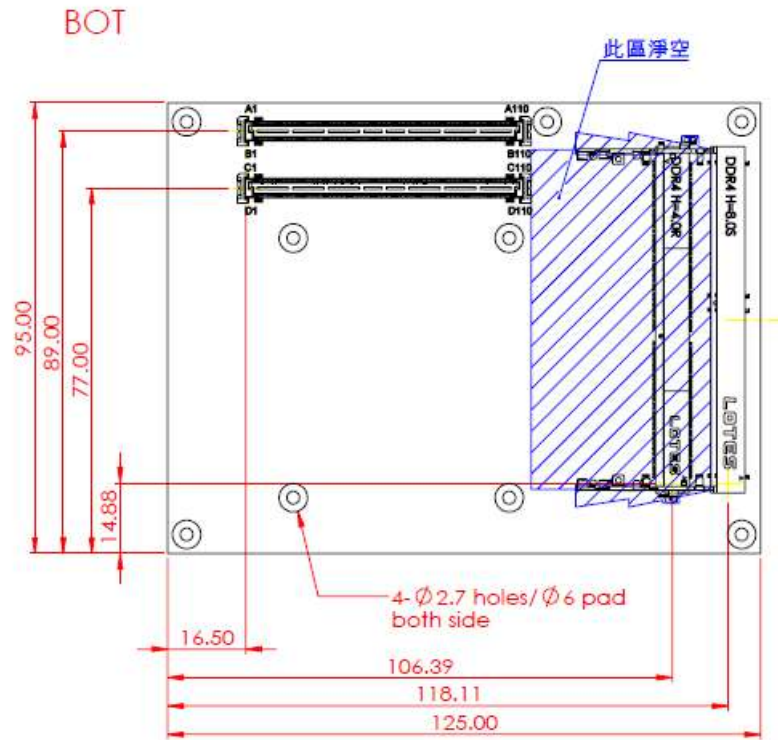


Figure 6 Mechanical Dimension - Bottom

## 3.6 4x SO-DIMM Slot Design

PCOM-B705GT has designed to support up to four DDR4 SO-DIMM slots up to 128GB capacities. Before inserting the DDR4 SO-DIMM memory, please refer to below connector arrangement. There are two SO-DIMM slots on channel 0, one slot on channel 1, and one slot on channel 2. The channel 2, SO-DIMM A (the bottom side, 8mm) only supported on 8C / 10C SOC SKUs.

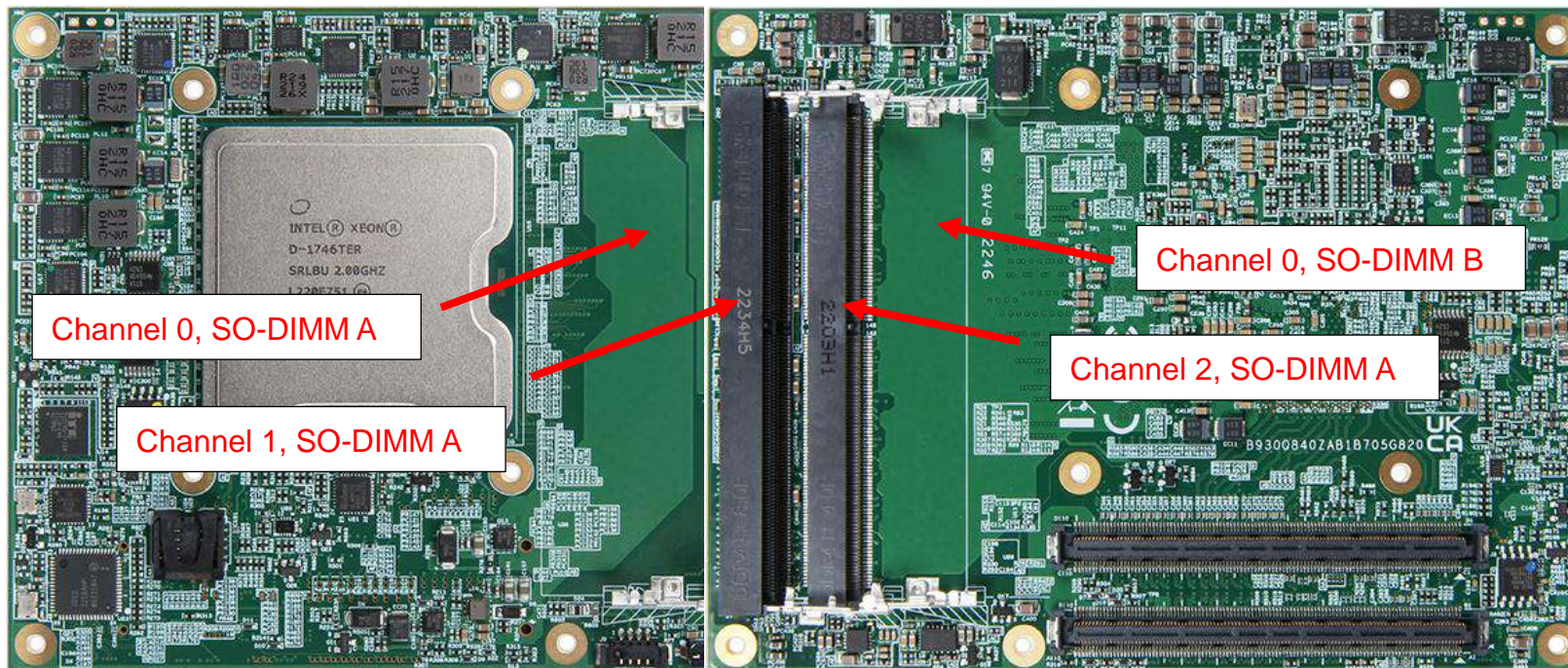


Figure 7 SO-DIMM slot design

## 3.7 Environmental Specifications

Storage Temperature	-40°C ~ 85°C
Operation Temperature	0°C ~ 60°C Extended : -40°C ~ 85°C (Selected SKU)
Storage Humidity	0% ~ 95%
Operation Humidity	0% ~ 95%

Table 5 Environmental Specifications



## 3.8 Ordering Guide

### ◆ Module

Product	Ordering P/N
PCOM-B705GT-D1746TER	AB1-3P64
PCOM-B705GT-D1735TR	AB1-3P65
PCOM-B705GT-D1732TE	AB1-3P66
PCOM-B705GT-D1715TE	AB1-3P67
PCOM-B705GT-D1712TR	AB1-3P68

Table 6 Ordering Guide - PCOM-B705GT

### ◆ Accessory

Accessory	Ordering P/N
Cooler	B9972180
Heatsink	B830C060
Heat-spreader	B830C070
Evaluation Carrier PCOM-C701-BMC	AB1-3K14Z

Table 7 Ordering Guide - Accessory

## 4 Thermal Solution

- Cooler Dimension

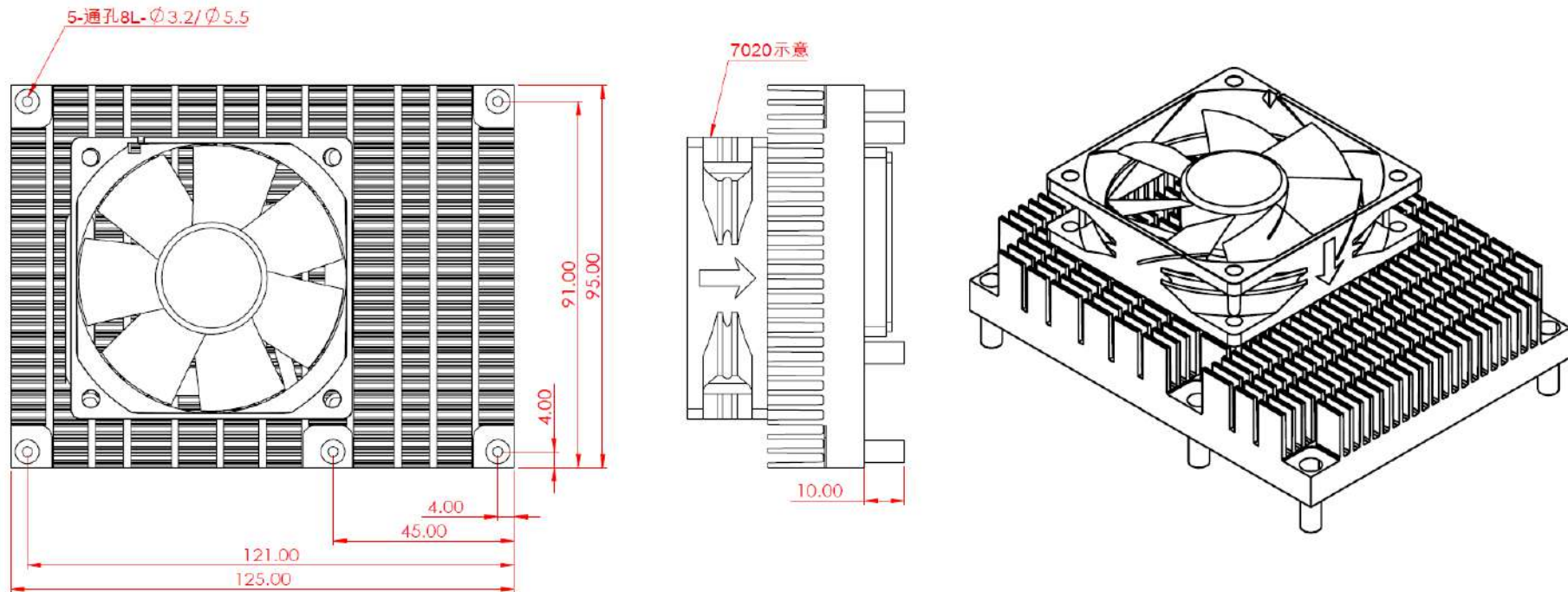


Figure 8 Cooler mechanical dimension

- Heatsink Dimension

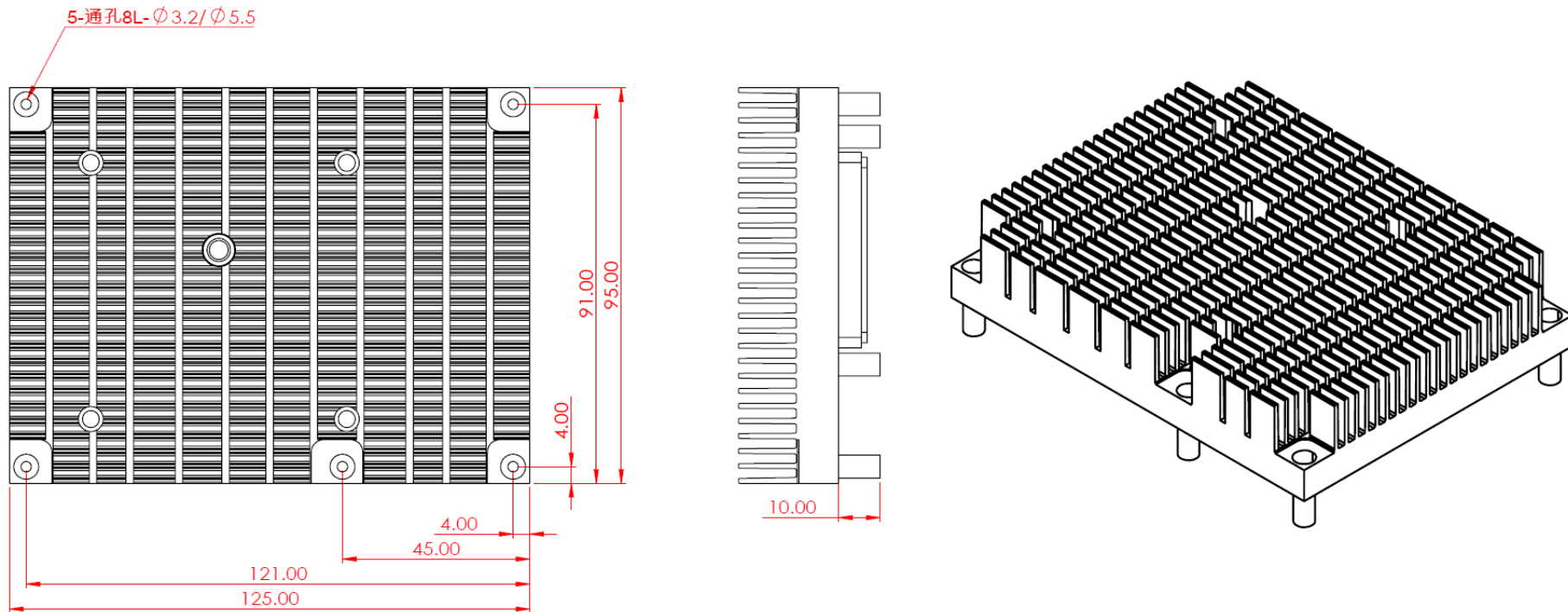


Figure 9 Heatsink mechanical dimension

- Heatspreader Dimension

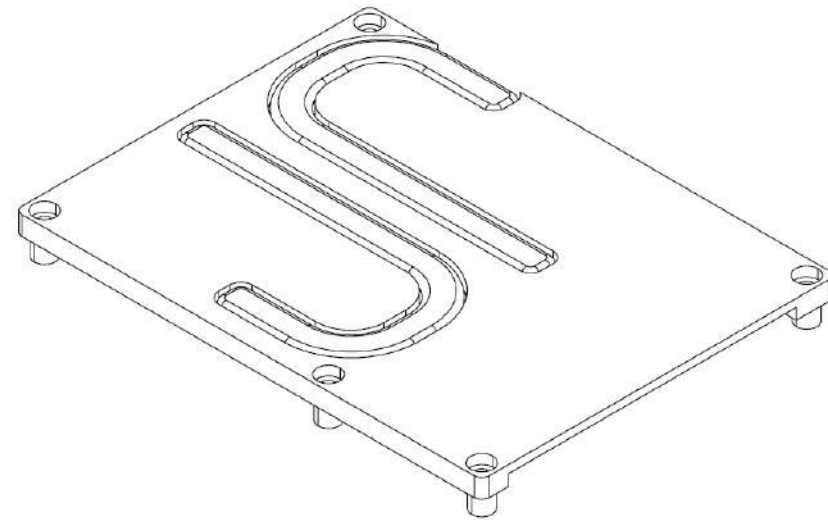
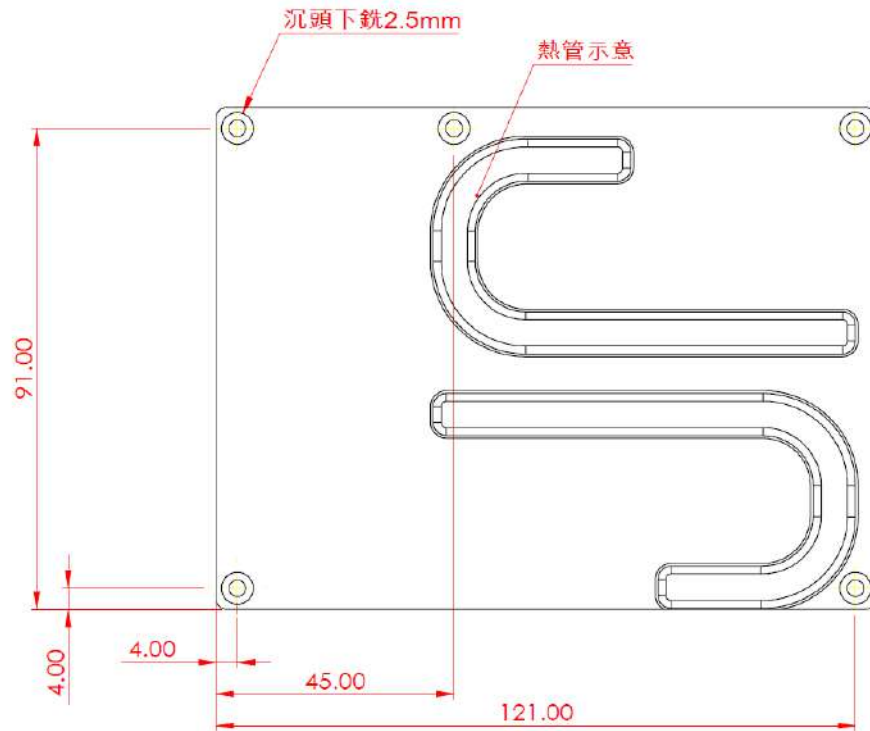


Figure 10 Heatspreader mechanical dimension

## 5 Pin out Tables

Pin	Row A	Row B	Row C	Row D
1	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
2	I210_MDI3_N	I210_ACT#	GND	GND
3	I210_MDI3_P	ESPI_CS1#_FRAME#	USB3_HSIO20_RXN0	USB3_HSIO20_TXN0
4	I210_100#	ESPI_IO0_LAD0	USB3_HSIO20_RXP0	USB3_HSIO20_TXP0
5	I210_1G#	ESPI_IO1_BMC	GND	GND
6	I210_MDI2_N	ESPI_IO2_LAD2	USB3_HSIO21_RXN1	USB3_HSIO21_TXN1
7	I210_MDI2_P	ESPI_IO3_LAD3	USB3_HSIO21_RXP1	USB3_HSIO21_TXP1
8	I210_LINK#	ESPI_ALERT1#_SIRQ	GND	GND
9	I210_MDI1_N	ESPI_ALERT#	USB3_HSIO22_RXN2	USB3_HSIO22_TXN2
10	I210_MDI1_P	ESPI_CLK_COME	USB3_HSIO22_RXP2	USB3_HSIO22_TXP2
11	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
12	I210_MDI0_N -	COME_PWRBTN#	USB3_HSIO23_RXN3	USB3_HSIO23_TXN3
13	I210_MDI0_P	SoC_SMB_SCL	USB3_HSIO23_RXP3	USB3_HSIO23_TXP3
14	GBE_CTREF_CN	SoC_SMB_SDA	GND	GND
15	KBC_SLPS3#	SoC_SMB_ALERT#	10PHY_MDC3_CN	10PHY_MDA3_CN
16	SATA_HSIO17_TXP17	SATA_HSIO18_TXP18	10PHY_MDC2_CN	10PHY_MDA2_CN
17	SATA_HSIO17_TXN17	SATA_HSIO18_TXN18	10G_PORT2_SDP	10G_PORT3_SDP
18	KBC_SLPS45#	ESPI_RESET#_SUSSTATE#	GND	GND
19	SATA_HSIO17_RXP17	SATA_HSIO18_RXP18	PCIE_HSIO6_RXP6	PCIE_HSIO6_TXP6
20	SATA_HSIO17_RXN17	SATA_HSIO18_RXN18	PCIE_HSIO6_RXN6	PCIE_HSIO6_TXN6
21	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)

22	PCIE_HSI015_TXP15	PCIE_HSI015_RXP15	PCIE_HSI07_RXP7	PCIE_HSI07_TXP7
23	PCIE_HSI015_TXN15	PCIE_HSI015_RXN15	PCIE_HSI07_RXN7	PCIE_HSI07_TXN7
24	KBC_SLPS45#	COME_PWROK	10G_INT2_CN	10G_INT3_CN
25	PCIE_HSI014_TXP14	PCIE_HSI014_RXP14	GND	GND
26	PCIE_HSI014_TXN14	PCIE_HSI014_RXN14	COME_ETH0_KR_RXDP3	ETH0_COME_KR_TXDP3
27	BATLOW_N	WDTO#	COME_ETH0_KR_RXDN3	ETH0_COME_KR_TXDN3
28	COMe_SATA_LED#	GND	GND	GND
29	NC	COME_PCl_e_GEN4_CLKP	COME_ETH0_KR_RXDP2	ETH0_COME_KR_TXDP2
30	NC	COME_PCl_e_GEN4_CLKN	COME_ETH0_KR_RXDN2	ETH0_COME_KR_TXDN2
31	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
32	RSVD	SPKR	NAC_I2C_SDA3	NAC_I2C_SCL3
33	RSVD	SMB1_CLK_COMe	NAC_I2C_SDA2	NAC_I2C_SCL2
34	BIOS_DIS0#	SMB1_DATA_COMe	10GPHY_RST23_CN	10GPHY_SEL23_CN
35	SoC_THERMTRIP#	COMe_THRM#	CEI_RST#	CEI_PRSNT#
36	PCIE_HSI013_TXP13	PCIE_HSI013_RXP13	10G_LED_SDA	NC
37	PCIE_HSI013_TXN13	PCIE_HSI013_RXN13	10G_LED_SCL	NC
38	GND	GND	NAC_I2C_SDA1	NAC_I2C_SCL1
39	PCIE_HSI012_TXP12	PCIE_HSI012_RXP12	CEI_SDA	CEI_SCL
40	PCIE_HSI012_TXN12	PCIE_HSI012_RXN12	10G_PORT0_SDP	10G_PORT1_SDP
41	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
42	SOC_USB2_DN2	SOC_USB2_DN3	COME_ETH0_KR_RXDP1	ETH0_COME_KR_TXDP1
43	SOC_USB2_DP2	SOC_USB2_DP3	COME_ETH0_KR_RXDN1	ETH0_COME_KR_TXDN1
44	COMe_USB_OC#	COMe_USB_OC#	GND	GND
45	SOC_USB2_DN0	SOC_USB2_DN1	10PHY_MDC1_CN	10PHY_MDA1_CN
46	SOC_USB2_DP0	SOC_USB2_DP1	CEI_MDC	CEI_MDIO
47	+V3P3_RTC	COME_ESPI_EN#	CEI_INT#	ETH_PHY_INT#

48	RSVD	NC	GND	GND
49	GBE0_SDP	COME_RSTBTN#	COME_ETH0_KR_RXDP0	ETH0_COME_KR_TXDP0
50	ESPI_ALERT1#_SIRQ	PLTRST2#	COME_ETH0_KR_RXDN0	ETH0_COME_KR_TXDN0
51	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
52	PCIE_HSIO5_TXP5	PCIE_HSIO5_RXP5	COME_SoC_PCIE4_RXDP0	SoC_COME_PCIE4_TXDP0
53	PCIE_HSIO5_TXN5	PCIE_HSIO5_RXN5	COME_SoC_PCIE4_RXDN0	SoC_COME_PCIE4_TXDN0
54	EC_GPI0	EC_GPO1	GND	GND
55	PCIE_HSIO4_TXP4	PCIE_HSIO4_RXP4	COME_SoC_PCIE4_RXDP1	SoC_COME_PCIE4_TXDP1
56	PCIE_HSIO4_TXN4	PCIE_HSIO4_RXN4	COME_SoC_PCIE4_RXDN1	SoC_COME_PCIE4_TXDN1
57	GND	EC_GPO2	TYPE1	GND
58	PCIE_HSIO3_TXP3	PCIE_HSIO3_RXP3	COME_SoC_PCIE4_RXDP2	SoC_COME_PCIE4_TXDP2
59	PCIE_HSIO3_TXN3	PCIE_HSIO3_RXN3	COME_SoC_PCIE4_RXDN2	SoC_COME_PCIE4_TXDN2
60	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
61	PCIE_HSIO2_TXP2	PCIE_HSIO2_RXP2	COME_SoC_PCIE4_RXDP3	SoC_COME_PCIE4_TXDP3
62	PCIE_HSIO2_TXN2	PCIE_HSIO2_RXN2	COME_SoC_PCIE4_RXDN3	SoC_COME_PCIE4_TXDN3
63	EC_GPI1	EC_GPO3	GND	GND
64	PCIE_HSIO1_TXP1	PCIE_HSIO1_RXP1	GND	GND
65	PCIE_HSIO1_TXN1	PCIE_HSIO1_RXN1	COME_SoC_PCIE4_RXDP4	SoC_COME_PCIE4_TXDP4
66	GND	WAKE0#_CN	COME_SoC_PCIE4_RXDN4	SoC_COME_PCIE4_TXDN4
67	EC_GPI2	WAKE1#_CN	RAPID_SUTDN	GND
68	PCIE_HSIO0_TXP0	PCIE_HSIO0_RXP0	COME_SoC_PCIE4_RXDP5	SoC_COME_PCIE4_TXDP5
69	PCIE_HSIO0_TXN0	PCIE_HSIO0_RXN0	COME_SoC_PCIE4_RXDN5	SoC_COME_PCIE4_TXDN5
70	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
71	PCIE_HSIO8_TXP8	PCIE_HSIO8_RXP8	COME_SoC_PCIE4_RXDP6	SoC_COME_PCIE4_TXDP6
72	PCIE_HSIO8_TXN8	PCIE_HSIO8_RXN8	COME_SoC_PCIE4_RXDN6	SoC_COME_PCIE4_TXDN6
73	GND	GND	GND	GND

74	PCIE_HSI09_TXP9	PCIE_HSI09_RXP9	COME_SoC_PCIE4_RXDP7	SoC_COME_PCIE4_TXDP7
75	PCIE_HSI09_TXN9-	PCIE_HSI09_RXN9	COME_SoC_PCIE4_RXDN7	SoC_COME_PCIE4_TXDN7
76	GND	GND	GND	GND
77	PCIE_HSI010_TXP10	PCIE_HSI010_RXP10	GND	GND
78	PCIE_HSI010_TXN10	PCIE_HSI010_RXN10	COME_SoC_PCIE4_RXDP8	SoC_COME_PCIE4_TXDP8
79	GND	GND	COME_SoC_PCIE4_RXDN8	SoC_COME_PCIE4_TXDN8
80	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
81	PCIE_HSI011_TXP11	PCIE_HSI011_RXP11	COME_SoC_PCIE4_RXDP9	SoC_COME_PCIE4_TXDP9
82	PCIE_HSI011_TXN11	PCIE_HSI011_RXN11	COME_SoC_PCIE4_RXDN9	SoC_COME_PCIE4_TXDN9
83	GND	GND	GND	GND
84	LAN_NCSI_TXEN	+5VSB	GND	GND
85	EC_GPI3	+5VSB	COME_SoC_PCIE4_RXDP10	SoC_COME_PCIE4_TXDP10
86	NC	+5VSB	COME_SoC_PCIE4_RXDN10	SoC_COME_PCIE4_TXDN10
87	GND	+5VSB	GND	GND
88	COMe_PCIE_GEN3_CLKP	BIOS_DIS1#	COME_SoC_PCIE4_RXDP11	SoC_COME_PCIE4_TXDP11
89	COMe_PCIE_GEN3_CLKN	NCSI_RX_ER_CN	COME_SoC_PCIE4_RXDN11	SoC_COME_PCIE4_TXDN11
90	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
91	SPI_PWR_CN	LAN_NCSI_CLKIN	COME_SoC_PCIE4_RXDP12	SoC_COME_PCIE4_TXDP12
92	COME_SPI_MISO	LAN_NCSI_RXD1	COME_SoC_PCIE4_RXDN12	SoC_COME_PCIE4_TXDN12
93	EC_GPO0	LAN_NCSI_RXD0	GND	GND
94	COME_SPI_CLK	LAN_NCSI_CRS_DV	COME_SoC_PCIE4_RXDP13	SoC_COME_PCIE4_TXDP13
95	COME_SPI_MOSI	LAN_NCSI_TXD1	COME_SoC_PCIE4_RXDN13	SoC_COME_PCIE4_TXDN13
96	TPM_PP	LAN_NCSI_TXD0	GND	GND
97	NC	COME_SPI_CS0#	GND	GND
98	SER0_TX	NCSI_ARB_IN	COME_SoC_PCIE4_RXDP14	SoC_COME_PCIE4_TXDP14
99	SER0_RX	NCSI_ARB_OUT	COME_SoC_PCIE4_RXDN14	SoC_COME_PCIE4_TXDN14



100	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
101	SER1_TX	FAN_PWMOUT_CN	COME_SoC_PCIE4_RXDP15	SoC_COME_PCIE4_TXDP15
102	SER1_RX	FAN_TACHIN_CN	COME_SoC_PCIE4_RXDN15	SoC_COME_PCIE4_TXDN15
103	LID#_CN	SLEEP#_CN	GND	GND
104	+V12_A	+V12_A	+V12_A	+V12_A
105	+V12_A	+V12_A	+V12_A	+V12_A
106	+V12_A	+V12_A	+V12_A	+V12_A
107	+V12_A	+V12_A	+V12_A	+V12_A
108	+V12_A	+V12_A	+V12_A	+V12_A
109	+V12_A	+V12_A	+V12_A	+V12_A
110	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)

Table 8 Pin out Description

## 6 BIOS Setup Items

PCOM-B705GT is equipped with the AMI BIOS stored in Flash ROM. These BIOS has a built-in setup program that allows users to modify the basic system configuration easily. This type of information is stored in SPI ROM so that it is retained during power-off periods. When system is turned on, PCOM-B705GT communicates with peripheral devices and checks its hardware resources against the configuration information stored in the BIOS. If any error is detected, or the BIOS parameters need to be initially defined, the diagnostic program will prompt the user to enter the SETUP program. Some errors are significant enough to abort the start up.

## 6.1 Entering Setup -- Launch System Setup

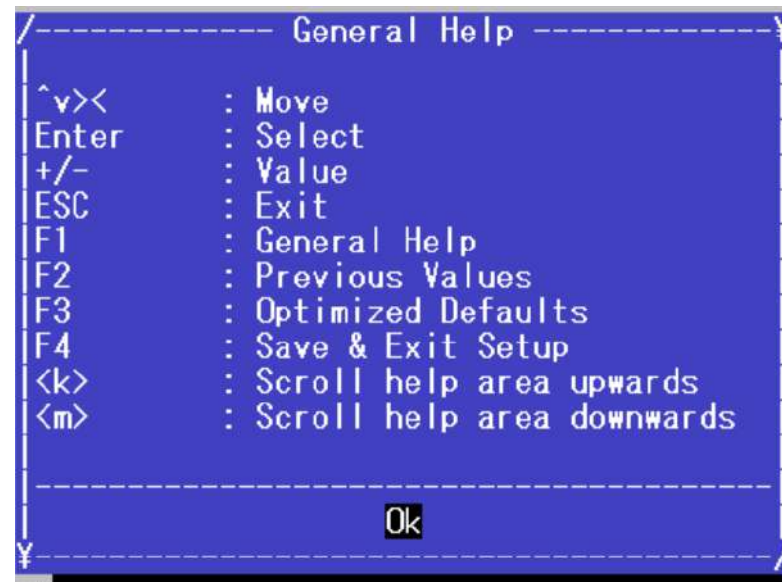
Power on the computer and the system will start POST (Power On Self Test) process. When the message below appears on the screen, press <Del> key will enter BIOS setup screen.

### Press <Del> to enter SETUP

If the message disappears before responding and still wish to enter Setup, please restart the system by turning it OFF and On or pressing the RESET button. It can be also restarted by pressing <Ctrl>, <Alt>, and <Delete> keys on keyboard simultaneously.

### Press <F1> to Run General Help or Resume

The BIOS setup program provides a General Help screen. The menu can be easily called up from any menu by pressing <F1>. The Help screen lists all the possible keys to use and the selections for the highlighted item. Press <Esc> to exit the Help screen.

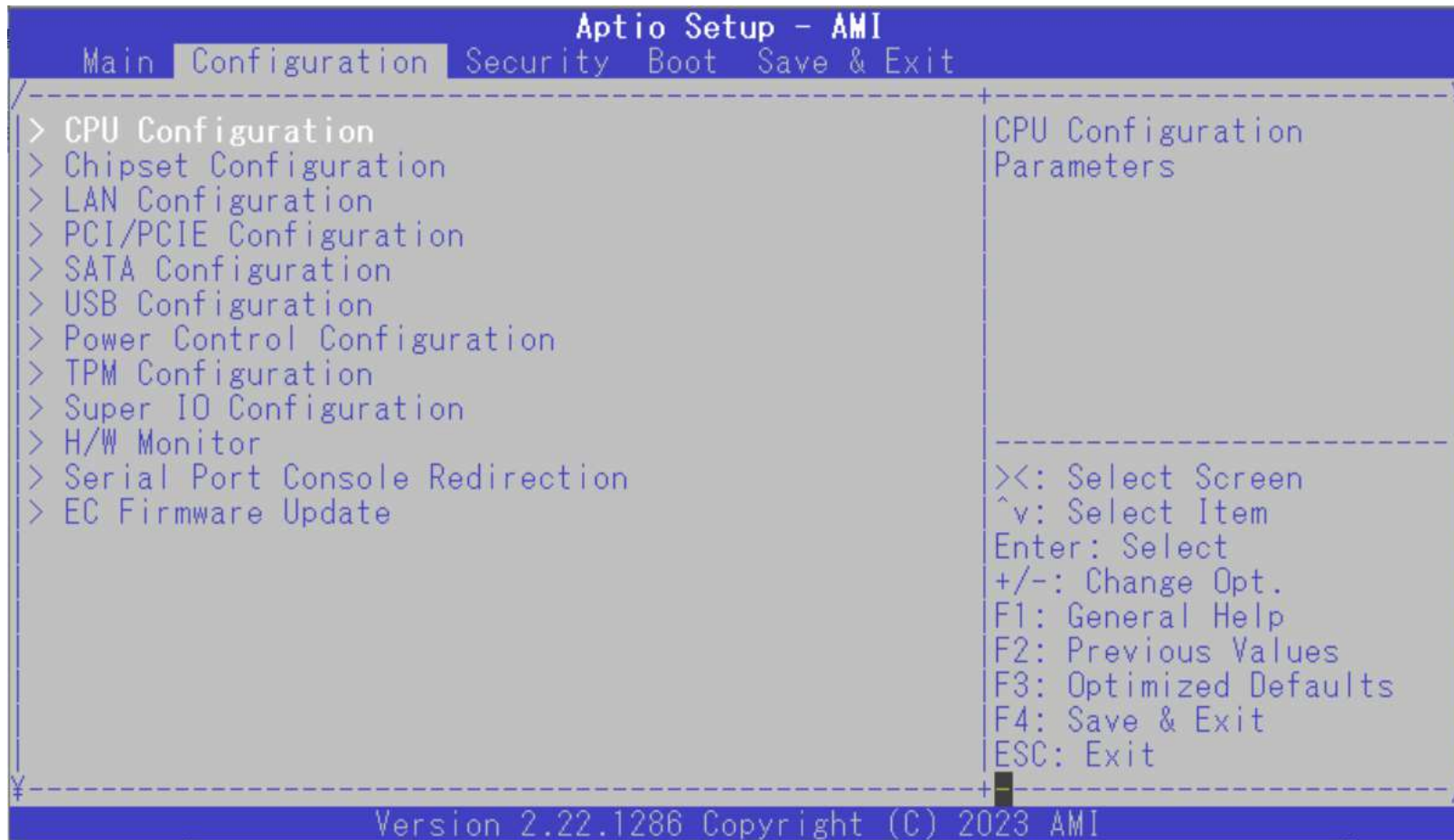


## 6.2 Main

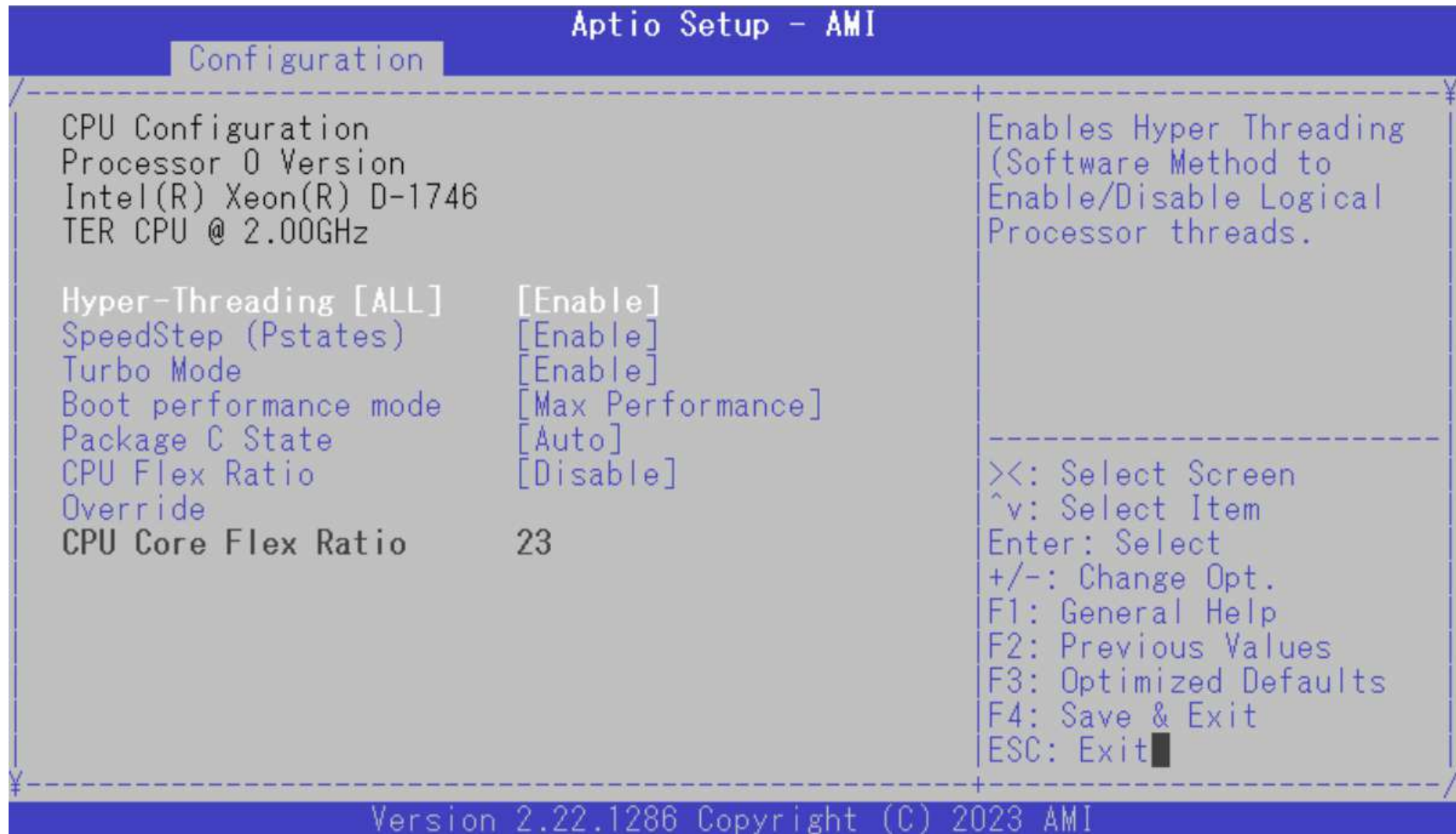


Feature	Description	Options
<b>System Language</b>		★English
<b>System Date</b>	The date format is <Day>, <Month> <Date> <Year>. Use [+] or [-] to configure system Date.	
<b>System Time</b>	The time format is <Hour> <Minute> <Second>. Use [+] or [-] to configure system Time.	

## 6.3 Configuration

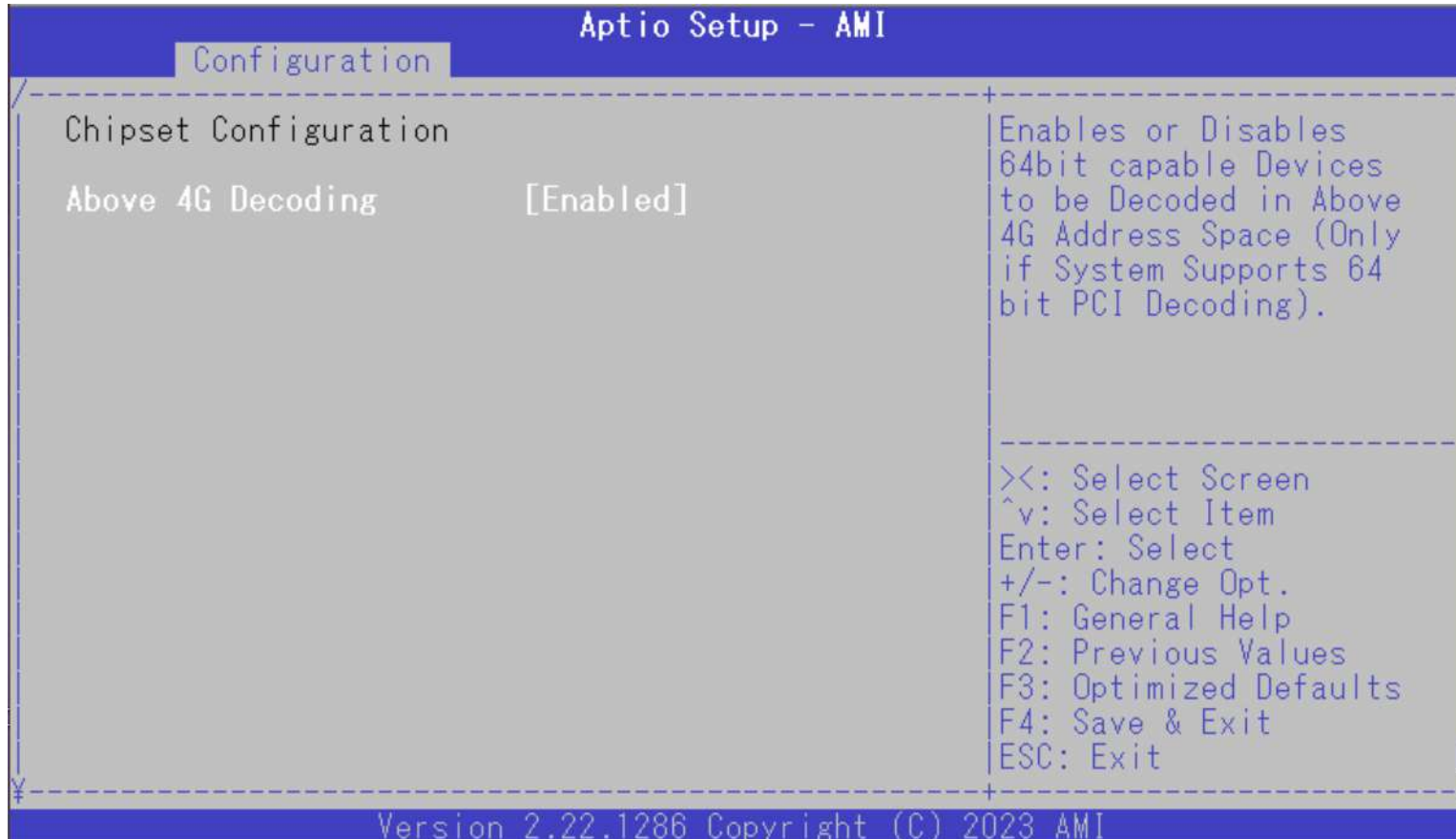


## CPU Configuration



Feature	Description	Options
<b>Hyper-Threading [ALL]</b>	Enables Hyper Threading (Software Method to Enable / Disable Logical Processor threads.)	Disabled, ★Enabled
<b>SpeedStep (Pstates)</b>	Enable/Disable EIST (P-States)	Disabled, ★Enabled
<b>Turbo Mode</b>	Enable/Disable processor Turbo Mode (requires EMTTM enabled too)	Disabled, ★Enabled
<b>Boot performance mode</b>	Select the performance state that the BIOS will set before OS hand off	★Max Performance, Max Efficient, Set by Intel Node Manager
<b>Package C State</b>	Package C State limit.	C0/C1 state, C2 state, C6(non Retention) state, ★Auto
<b>CPU Flex Ratio Override</b>	Enable/Disable CPU Flex Ratio Programming	★Disabled, Enabled
<b>CPU Core Flex Ratio</b>	Non-Turbo Mode Processor Core Ratio Multiplier	

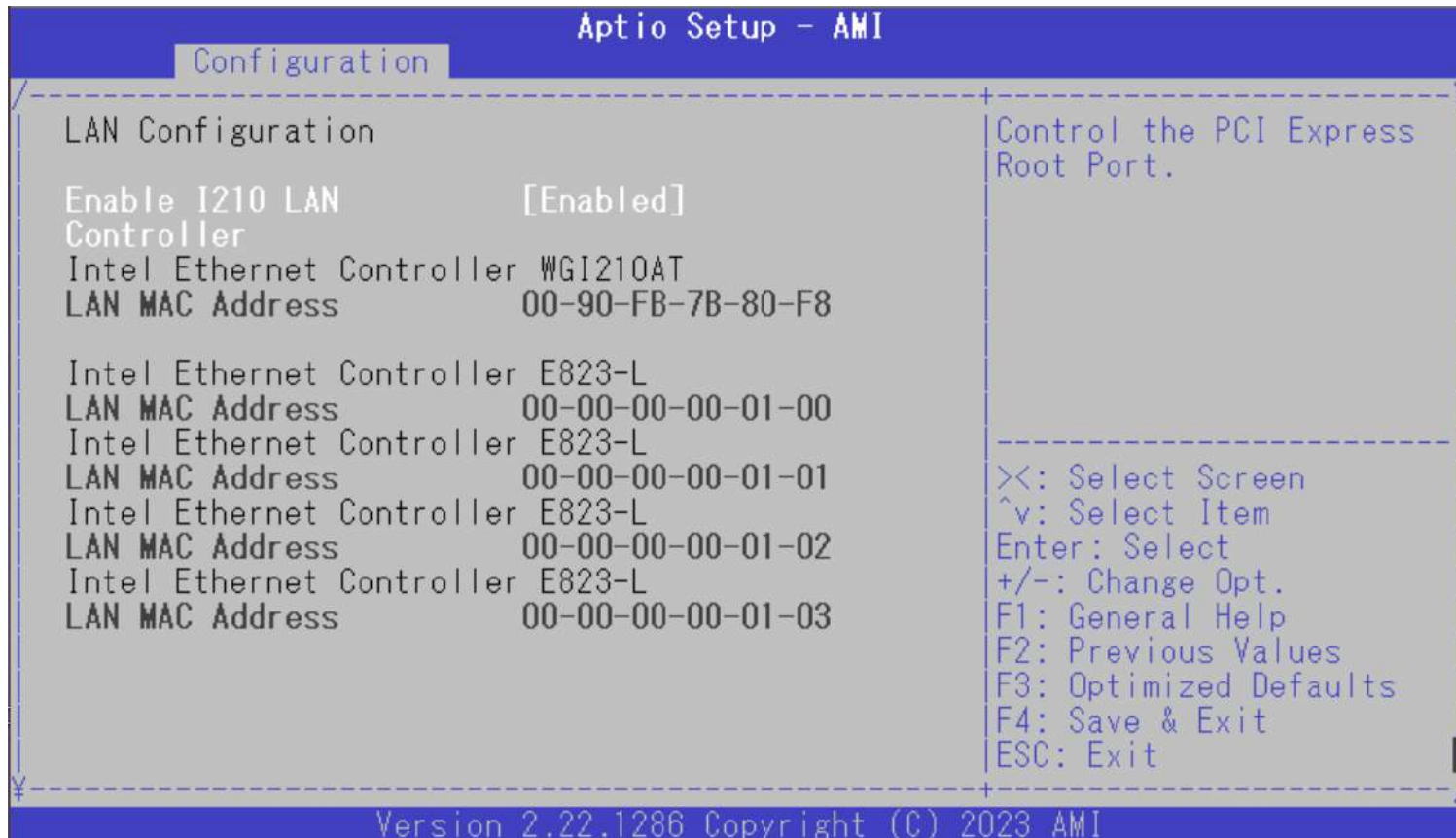
## Chipset Configuration



Feature	Description	Options
<b>Above 4G Decoding</b>	Enables or Disables 64bit capable Devices to be Decoded in Above 4G Address Space (Only if System Supports 64 bit PCI Decoding).	Disabled, ★Enabled

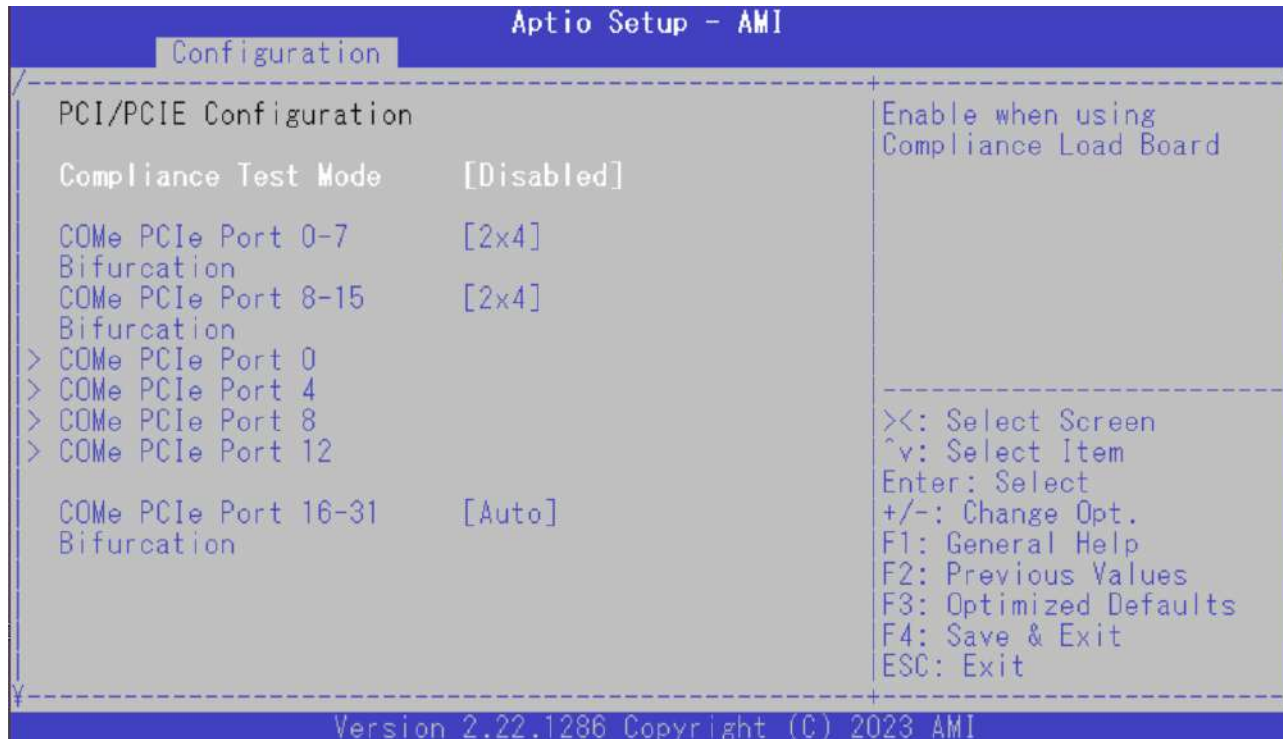


## LAN Configuration

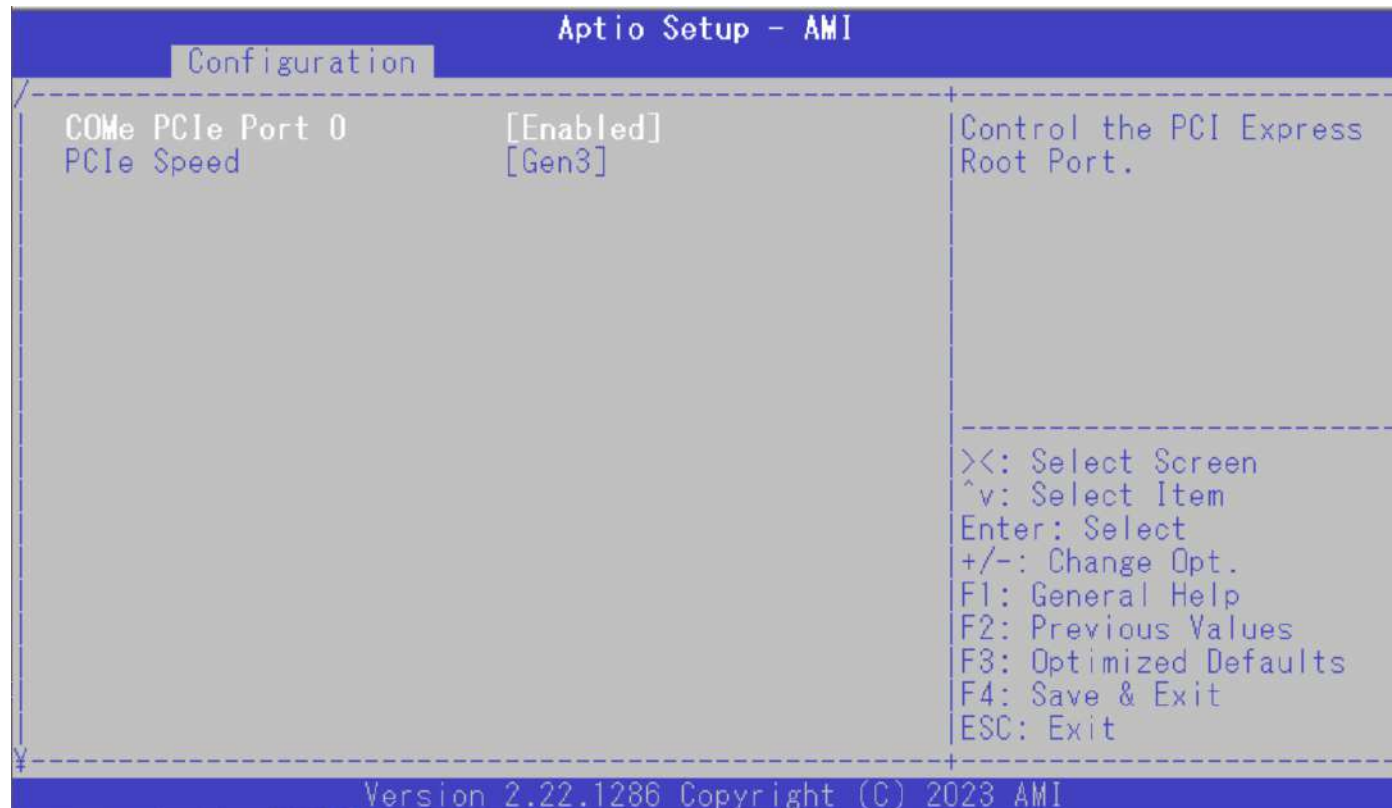


Feature	Description	Options
Enabled I210 LAN Control	Control the PCI Express Root Port.	Disabled, ★Enabled

## PCI/PCIE Configuration



Feature	Description	Options
<b>Compliance Test Mode</b>	Enable when using Compliance Load Board	★Disabled, Enabled
<b>COMe PCIe Port 0-7 Bifurcation</b>	Allows changing PCIe bifurcation	4x2, 1x4 2x2, 2x2 1x4, ★2x4, 1x8
<b>COMe PCIe Port 8-15 Bifurcation</b>	Allows changing PCIe bifurcation	4x2, 1x4 2x2, 2x2 1x4, ★2x4, 1x8
<b>COMe PCIe Port 0/4/8/12</b>	PCI Express Root Port Settings	
<b>COMe PCIe Port 16-31 Bifurcation</b>	Selects PCIe port Bifurcation for selected slot(s)	★Auto, x4x4x4x4, x4x4x8, x8x8, x16

➤ **COMe PCIe Port 0/4/8/12**

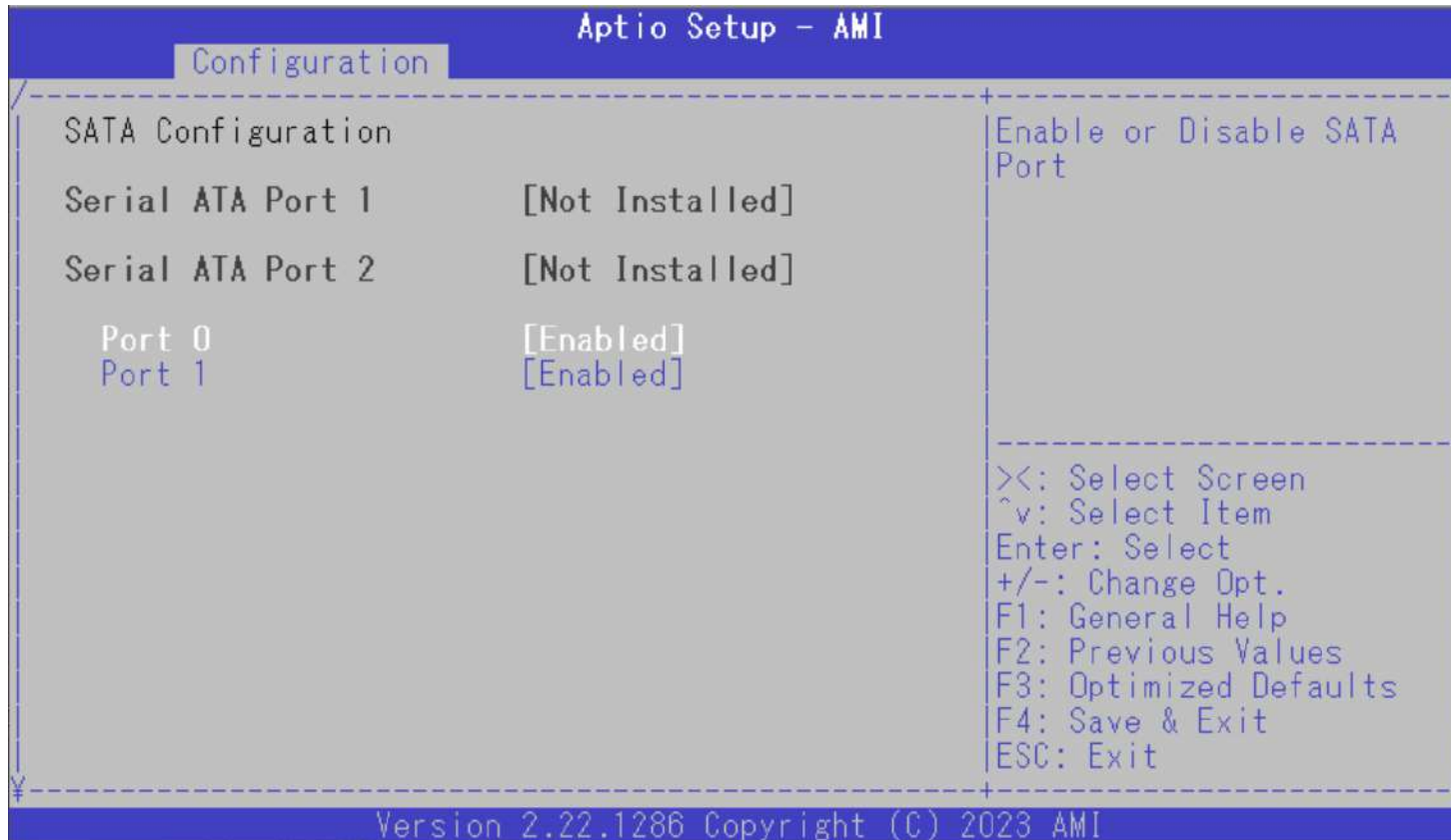
Feature	Description	Options
<b>COMe PCIe Port 0/4/8/12</b>	Control the PCI Express Root Port	Disabled, ★Enabled
<b>PCIe Speed</b>	Configure PCIe Speed Auto is equal to Gen2 or Gen3 depending on DTR soft strap	Gen1, Gen2, ★Gen3

➤ **COMe PCIe Port 16~31**



Feature	Description	Options
<b>PCI-E Port</b>	In auto mode the BIOS will remove the EXP port if there is no device or errors on that device and the device is not HP capable. Enable/Disable is used to enable/disable the port and expose/hide its CFG space.	★Auto, Disabled, Enabled
<b>Link Speed</b>	Choose Link Speed for this PCIe port.	★Auto Gen 1 (2.5 GT/s), Gen 2 (5 GT/s), Gen 3 (8 GT/s), Gen 4 (16 GT/s)

## SATA Configuration



Feature	Description	Options
<b>Port 0</b>	Enabled or Disabled SATA Port	Disabled, ★Enabled
<b>Port 1</b>	Enabled or Disabled SATA Port	Disabled, ★Enabled

## USB Configuration

```

Aptio Setup - AMI
-----
Configuration
-----
USB Configuration
USB Controllers:
  1 XHCI
USB Devices:
  1 Keyboard

Legacy USB Support      [Enabled]
XHCI Hand-off          [Enabled]
USB Mass Storage Driver Support [Enabled]

USB hardware delays and time-outs:
USB transfer time-out   [20 sec]
Device reset time-out  [20 sec]
Device power-up delay  [Auto]

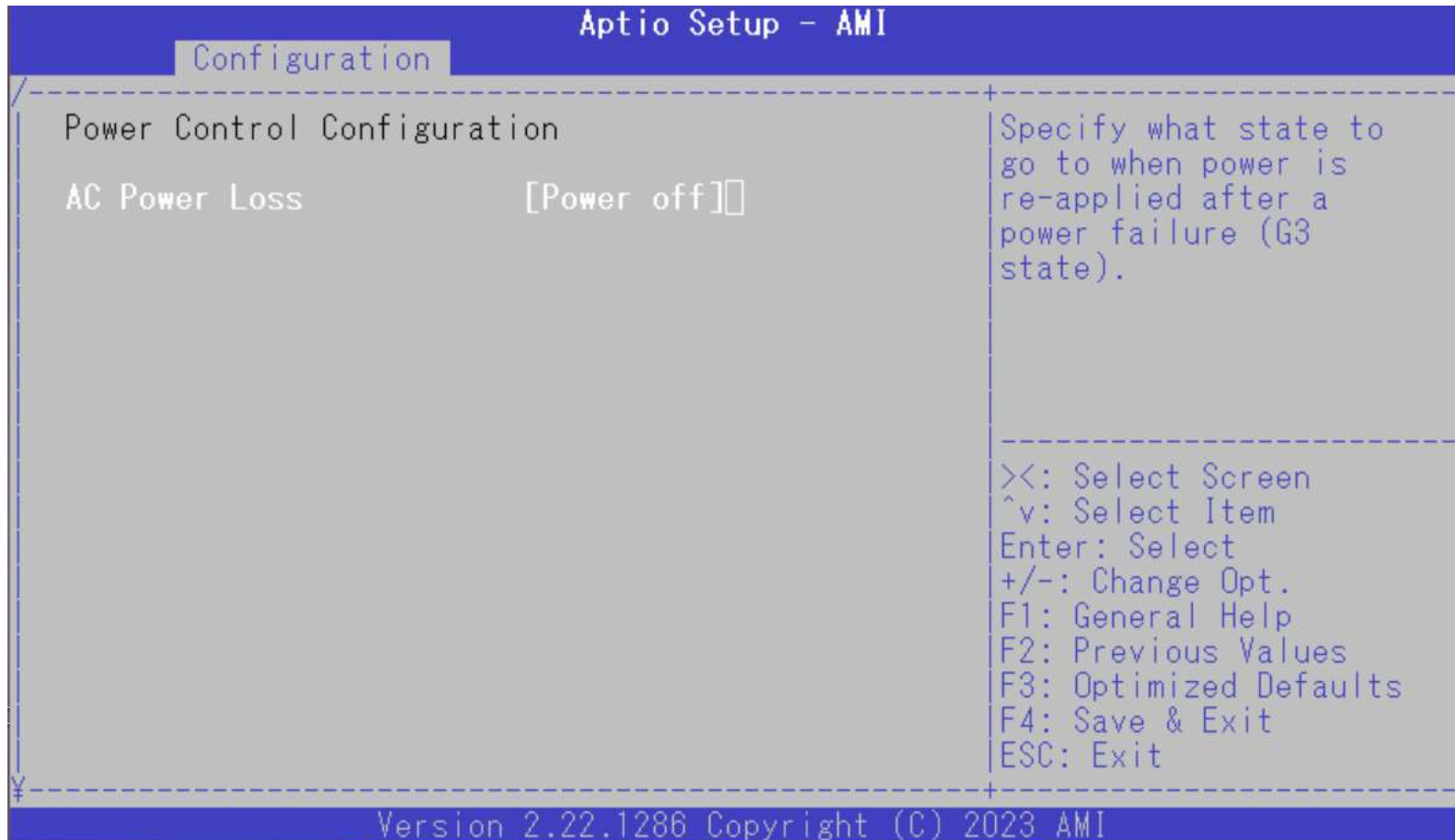
^ Enables Legacy USB
* support. AUTO option
* disables legacy support
* if no USB devices are
* connected. DISABLE
* option will keep USB
* devices available only
* for EFI applications.
*
* -----
* ><: Select Screen
* ^v: Select Item
* Enter: Select
* +/-: Change Opt.
* F1: General Help
* F2: Previous Values
+ F3: Optimized Defaults
v F4: Save & Exit
ESC: Exit

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```

Feature	Description	Options
<b>Legacy USB Support</b>	Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected.	★Enabled , Disabled, Auto
<b>XHCI Hand-off</b>	This is a workaround for OSES without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver	Disabled, ★Enabled
<b>USB Mass Storage Driver Support</b>	Enable/Disable USB Mass Storage Driver Support	Disabled, ★Enabled
<b>USB transfer time-out</b>	The time-out value for Control, Bulk, and Interrupt transfers.	1, 5, 10, ★20 sec
<b>Device reset time-out</b>	USB mass storage device Start Unit command time-out.	10, ★20, 30, 40 sec
<b>Device power-up delay</b>	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken.	★Auto, Manual

## Power Control Configuration



Feature	Description	Options
<b>AC Power Loss</b>	Specify what state to go to when power is re-applied after a power failure (G3 state)	Power On, ★Power Off



## TPM Configuration

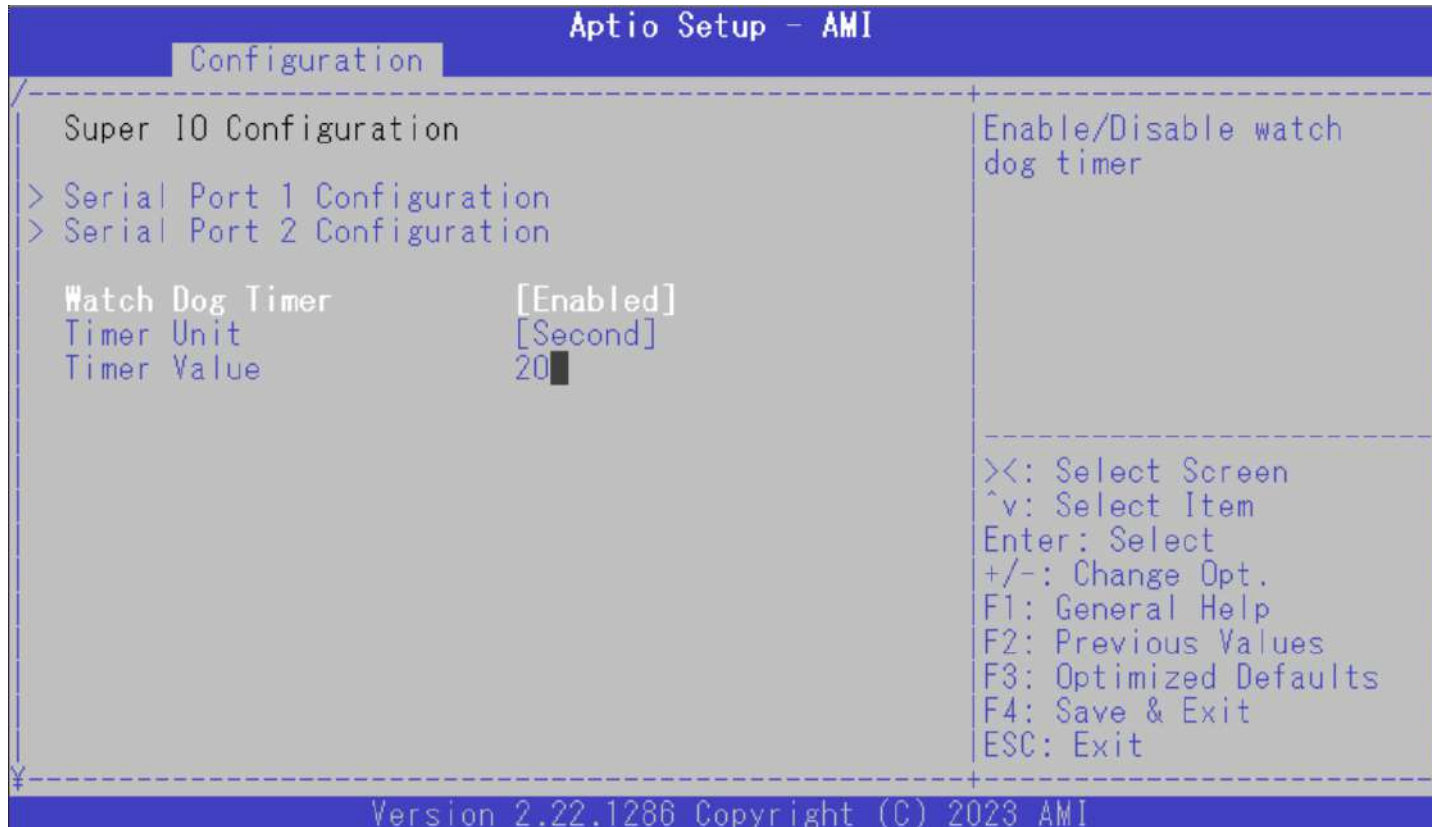
```

Aptio Setup - AMI
-----
Configuration
-----
TPM 2.0 Device Found
Firmware Version: 13.11
Vendor: IFX
TPM v1.2 Support [Enable]
Active PCR banks SHA256
Available PCR banks SHA256
SHA256 PCR Bank [Enabled]
Pending operation [None]
Platform Hierarchy [Enabled]
Storage Hierarchy [Enabled]
Endorsement [Enabled]
Hierarchy [ ]
TPM 2.0 UEFI Spec [TCG_2]
Version
Physical Presence [1.3]
Spec Version
TPM 2.0 [TIS]
InterfaceType [ ]
Device Select [Auto]
-----
^ Enables or Disables
* BIOS support for
* security device. O.S.
* will not show Security
* Device. TCG EFI
* protocol and INT1A
* interface will not be
* available.
*
* -----
* ><: Select Screen
* ^v: Select Item
* Enter: Select
* +/-: Change Opt.
+ F1: General Help
+ F2: Previous Values
+ F3: Optimized Defaults
v F4: Save & Exit
*
* -----
* +/-: Change Opt.
* F1: General Help
* F2: Previous Values
* F3: Optimized Defaults
v F4: Save & Exit
ESC: Exit
-----
Version 2.22.1286 Copyright (C) 2023 AMI

```

Feature	Description	Options
<b>TPM v1.2 Support</b>	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A Interface will not be available.	★Enabled, Disabled
<b>SHA256 PCR Bank</b>	Enable or Disable SHA256 PCR Bank.	Disabled, ★Enabled
<b>Pending operation</b>	Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device	★None, TPM Clear
<b>Platform Hierarchy</b>	Enable or Disable Platform Hierarchy.	Disabled, ★Enabled
<b>Storage Hierarchy</b>	Enable or Disable Storage Hierarchy	Disabled, ★Enabled
<b>Endorsement Hierarchy</b>	Enable or Disable Endorsement Hierarchy.	Disabled, ★Enabled
<b>TPM 2.0 UEFI Spec Version</b>	Select the TCG2 Spec Version Support, TCG_1_2: the Compatible mode for Win8/Win10, TCG_2: Support new TCG2 protocol and event format for Win10 or later.	TCG_1_2, ★TCG_2
<b>Physical Presence Spec Version</b>	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3	1.2, ★1.3
<b>Device Select</b>	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated.	TPM 1.2, TPM 2.0, ★Auto

## Super IO Configuration



Feature	Description	Options
<b>Watch Dog Timer</b>	Enable/Disable Watch Dog Timer	★Disabled, Enabled
<b>Timer Unit</b>	Select Timer count unit of WDT [Watch Dog Timer [Enabled]]	★Second, Minute
<b>Timer value</b>	Set WDT Timer value seconds / minutes [Watch Dog Timer [Enabled]]	★20

➤ **Serial Port 1 Configuration**



Feature	Description	Options
<b>Module Serial Port 1</b>	Enable or Disable Serial Port (COM)	★Enabled, Disabled
<b>Change Settings</b>	Select an optimal settings for Super IO Device	★Auto ,IO=3F8h; IRQ=4; IO=3F8h; IRQ=3,4,10,11; IO=2F8h; IRQ=3,4,10,11; IO=3E8h; IRQ=3,4,10,11; IO=2E8h; IRQ=3,4,10,11;

### ➤ Serial Port 2 Configuration



Feature	Description	Options
<b>Module Serial Port 2</b>	Enable or Disable Serial Port (COM)	★Enabled, Disabled
<b>Change Settings</b>	Select an optimal settings for Super IO Device	★Auto ,IO=3E8h; IRQ=3; IO=3F8h; IRQ=3,4,10,11; IO=2F8h; IRQ=3,4,10,11; IO=3E8h; IRQ=3,4,10,11; IO=2E8h; IRQ=3,4,10,11;

## H/W Monitor

```
Aptio Setup Utility - Copyright (C) 2021 American Megatrends, Inc.
  Configuration

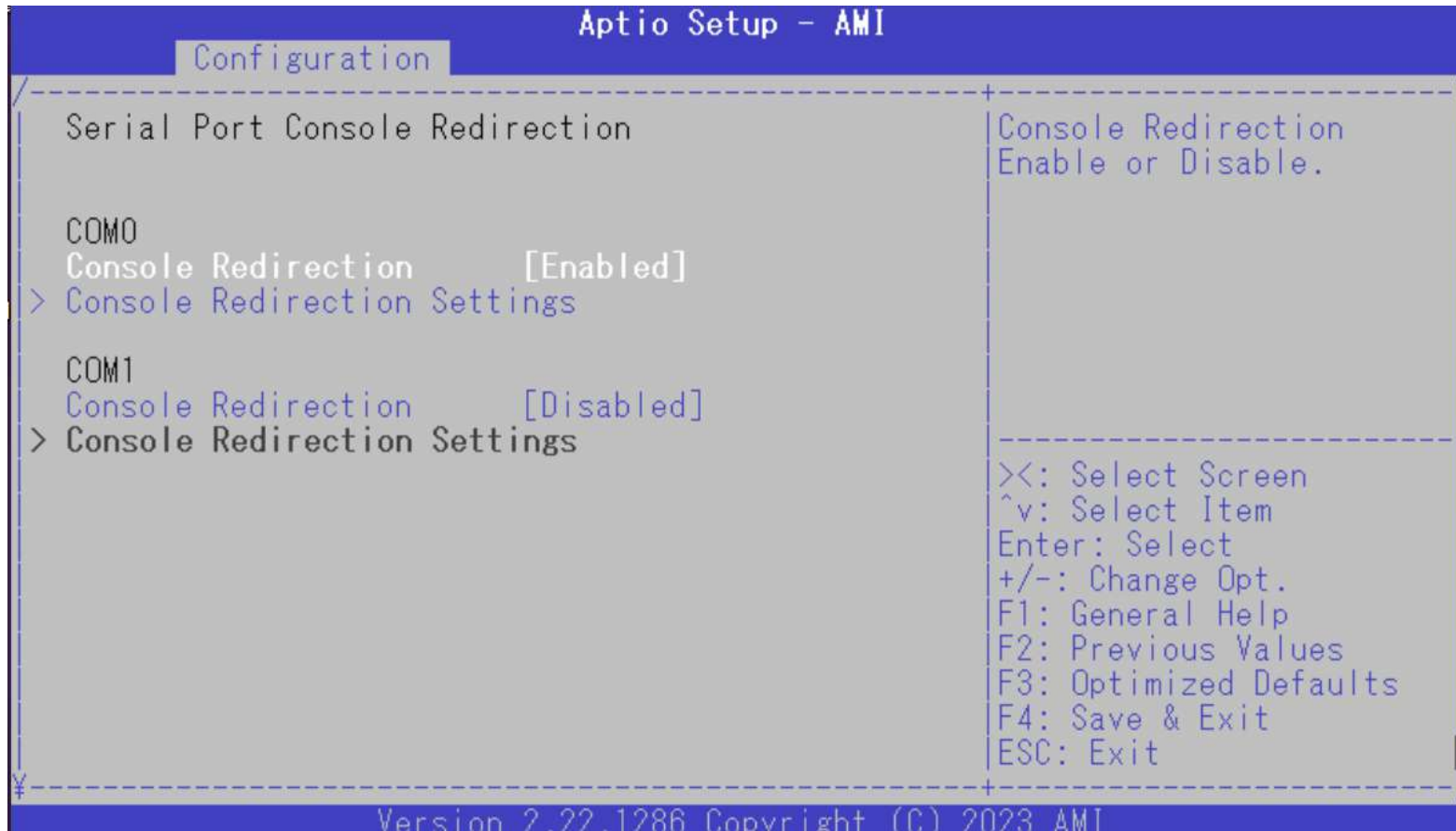
CPU temperature      : +36 °C
Fan1 Speed          : 6738 RPM

Vcore               : +1.824 V
+3.3V              : +3.360 V
+5V                 : +5.107 V
+12V               : +12.513 V
VDIMM              : +1.242 V

++: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit

Version 2.20.1271. Copyright (C) 2021 American Megatrends, Inc.
```

## Serial Port Console Redirection



Feature	Description	Options
<b>COM0 Console Redirection</b>	Console Redirection Enable or Disable	Disabled, ★Enabled
<b>COM1 Console Redirection</b>	Console Redirection Enable or Disable	★Disabled, Enabled

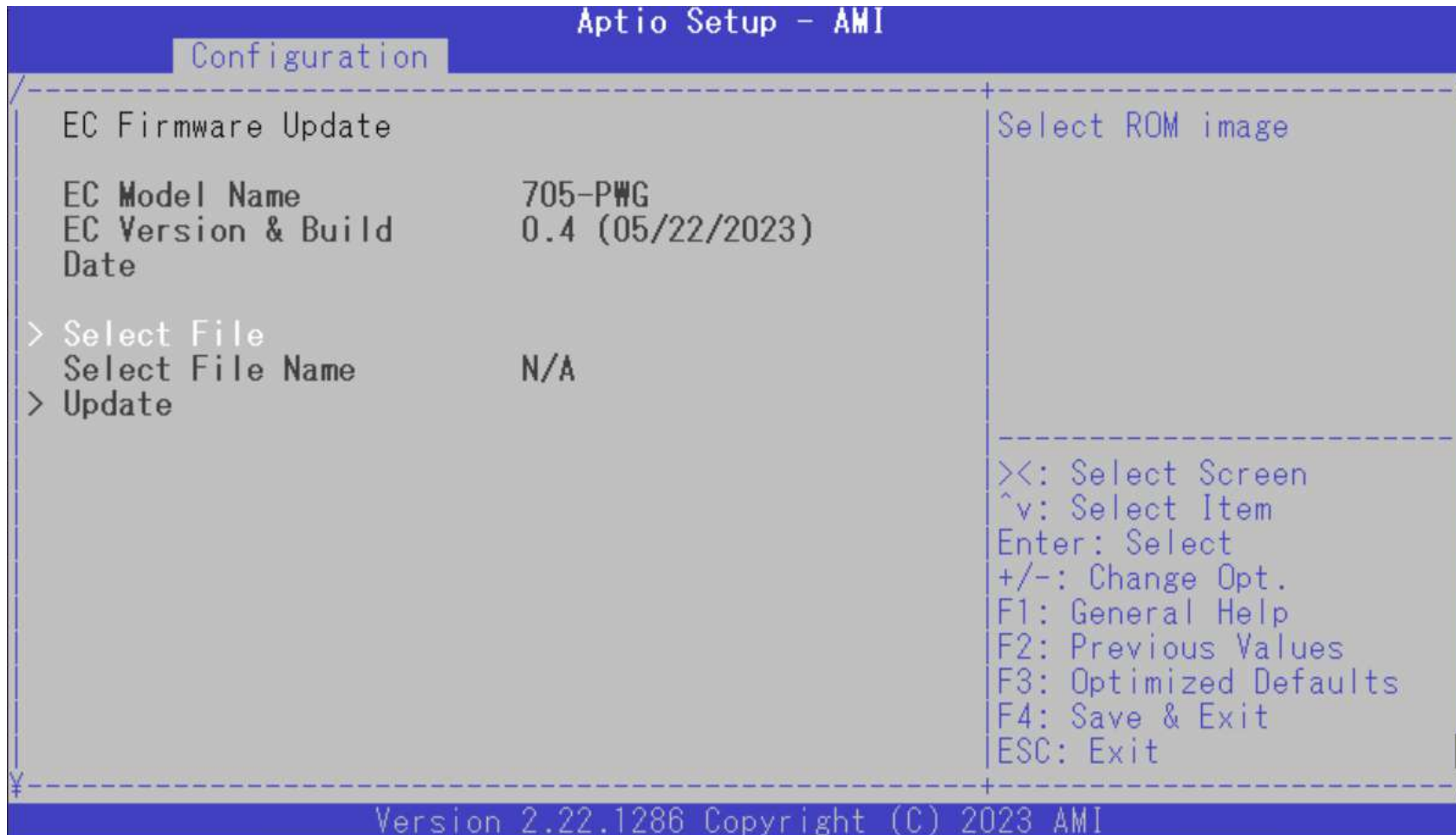
## ➤ Console Redirection Settings





Feature	Description	Options
<b>Terminal Type</b>	Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color , function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.	★VT100+, VT100, ANSI, VT-UTF8
<b>Bits per second</b>	Select Serial port transmission speed. The speed must be matched on other side. Long or noisy lines may require lower speeds.	★115200, 9600, 19200, 38400, 57600
<b>Data bits</b>	Data bits	★8, 7
<b>Parity</b>	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.	★None, Even, Odd, Mark, Space
<b>Stop Bits</b>	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.	★1,2
<b>Flow Control</b>	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signal.	★None, Hardware RTS/CTS
<b>VT-UTF8 Combo Key Support</b>	Enable VT-UTF8 Combination Key Support for ANSI / VT100 terminals	★Enabled, Disabled
<b>Recorder Mode</b>	With this mode enabled only text will be sent. This is to capture Terminal data.	★Disabled, Enabled
<b>Resolution 100x31</b>	Enables or disables extended terminal resolution	★Disabled, Enabled
<b>Putty KeyPad</b>	Select Function Key and KeyPad on Putty.	★VT100, LINUX, XTERMR6, SCO, ESCN, VT400

## EC Firmware Update



## 6.4 Security

```

Aptio Setup - AMI
Main Configuration Security Boot Save & Exit
-----
is a power on password and must be entered to
boot or enter Setup. In Setup the User will
have Administrator rights.
The password length must be
in the following range:
Minimum length          3
Maximum length         20

Password Check Mode    [Setup]

Administrator Password
User Password

> Secure Boot

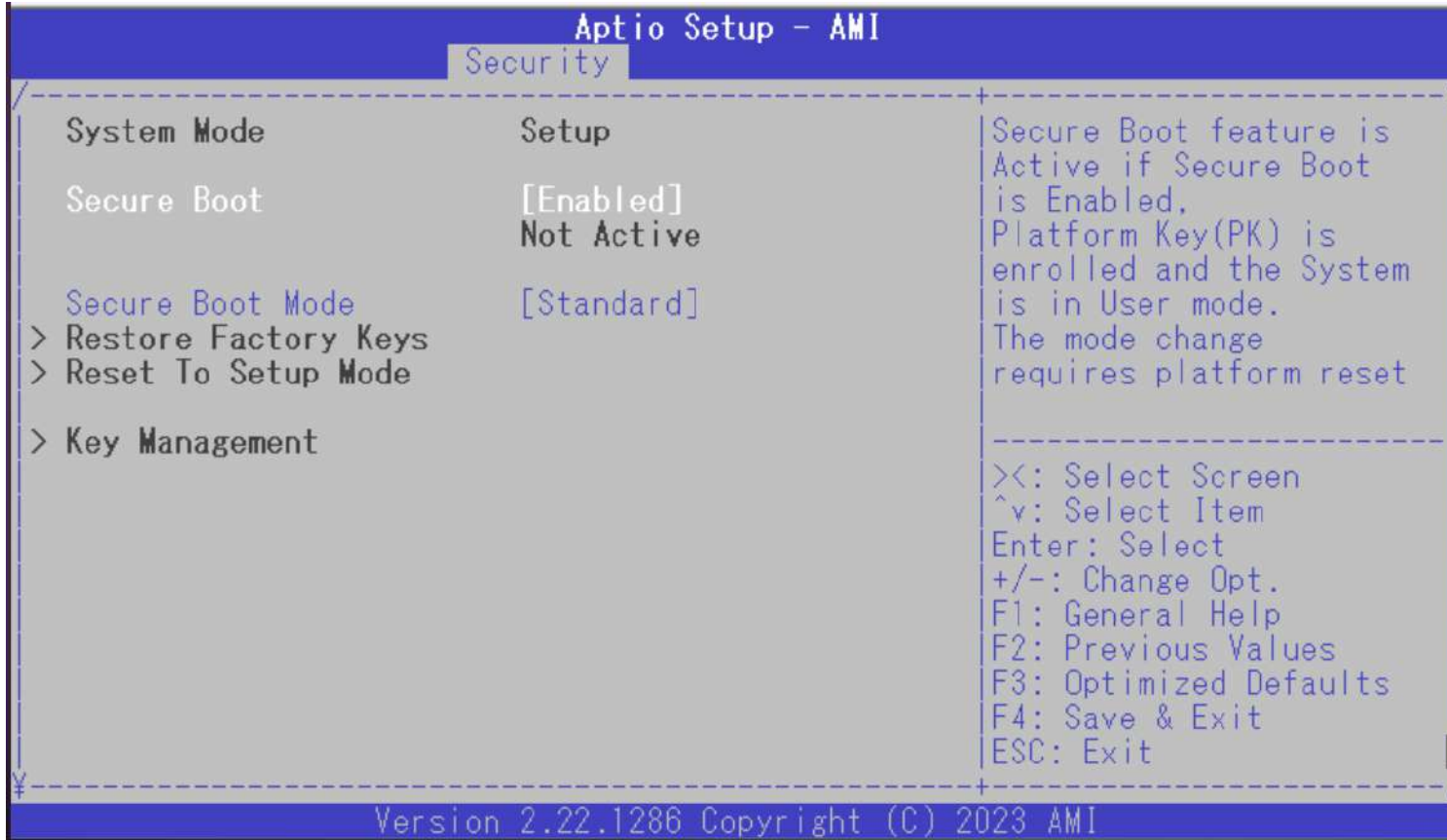
^ Secure Boot
+ configuration
+
+
+
*
*
*
*
*
*
* -----
* ><: Select Screen
* ^v: Select Item
* Enter: Select
* +/-: Change Opt.
* F1: General Help
* F2: Previous Values
* F3: Optimized Defaults
v F4: Save & Exit
|ESC: Exit

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```

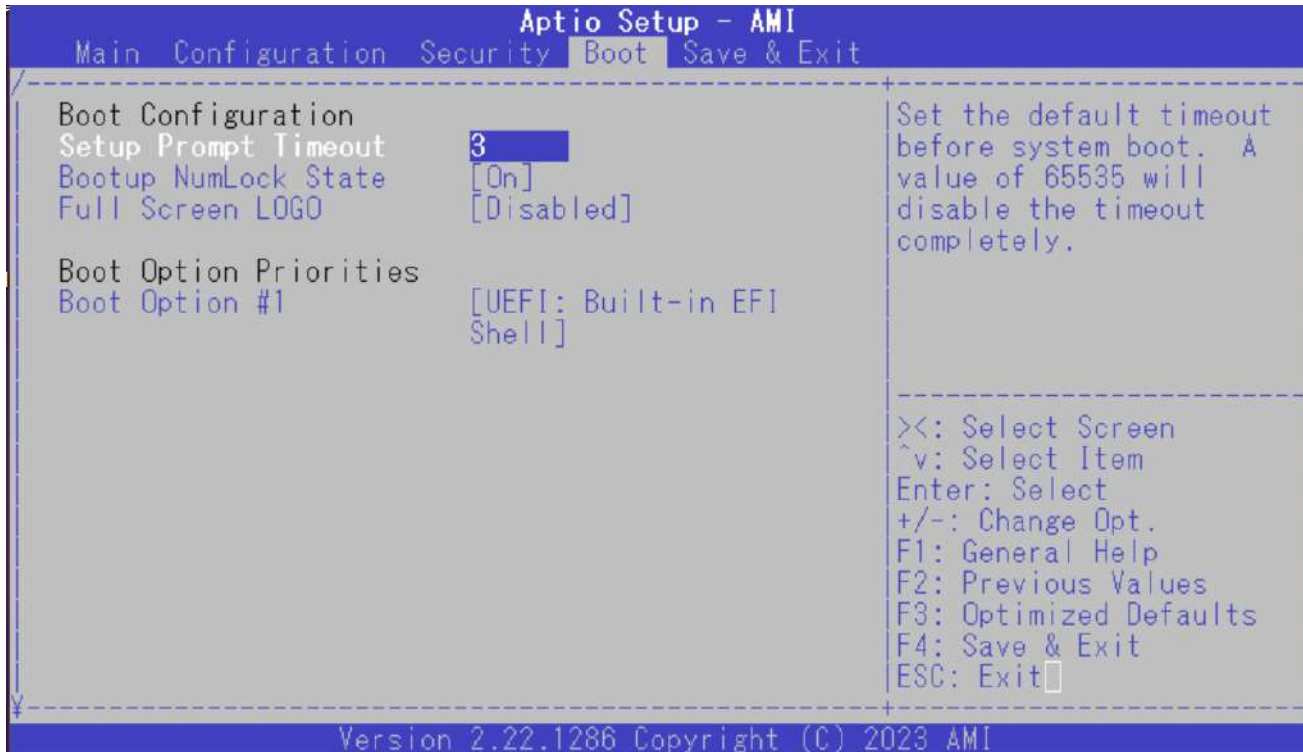
Feature	Description	Options
<b>Password Check Mode</b>	[Setup] check password when enter setup screen. [Power on] check password on every time system power on.	★Setup, Power On
<b>Administrator Password</b>	Set Administrator Password	

➤ **Secure Boot**



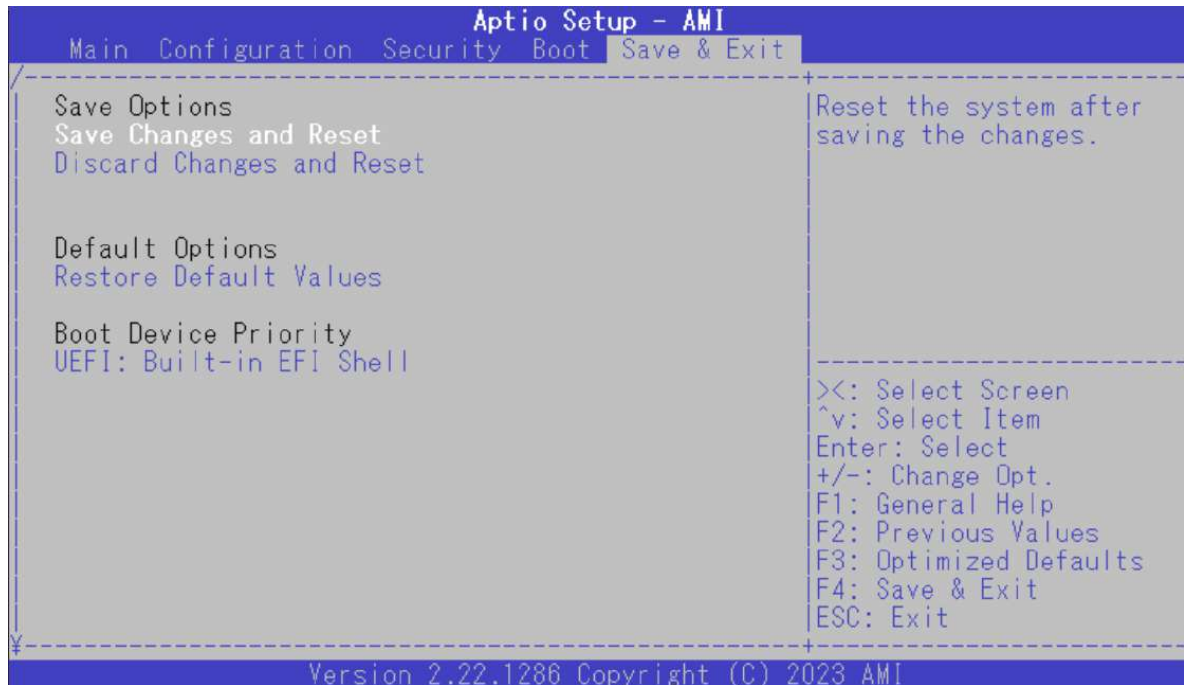
Feature	Description	Options
<b>Secure Boot</b>	Secure Boot feature is Active if Secure Boot is Enable, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset	Disabled, ★Enabled
<b>Secure Boot Mode</b>	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full	★Standard, Custom

## 6.5 Boot



Feature	Description	Options
<b>Setup Prompt Timeout</b>	Set the default timeout before system boot. A value of 65535 will disable the timeout completely.	★3
<b>Bootup NumLock State</b>	Select the keyboard NumLock state.	★On, Off
<b>Full Screen LOGO</b>	Enables or disables Quiet Boot option and Full screen Logo.	★Disabled, Enabled
<b>Boot Option #</b>	Sets the system boot order	UEFI: Built-in EFI Shell Disabled

## 6.6 Save & Exit



Feature	Description	Options
<b>Save Changes and Reset</b>	Reset the system after saving the changes.	
<b>Restore Defaults Values</b>	Restore/Load Default values for all the setup options.	
<b>UEFI: Built-in EFI Shell</b>	Reset the system after saving the changes. (Boot option filter: UEFI only)	

## 7 System Resources

### LPC

Device	I/O Address	Note
Embedded Controller	0x6E / 0x6F	EC Address
	0x62 / 0x66	EC ACPI CMD Port
	0x200 / 0x201	EC BRAM Port for I2C function
	0x300~0x3FF	EC LPC IO Space
	0x3F8~0x3FF	EC UART1
	0x3E8~0x3EF	EC UART2
Carrier SIO	N/A	

### SMBUS

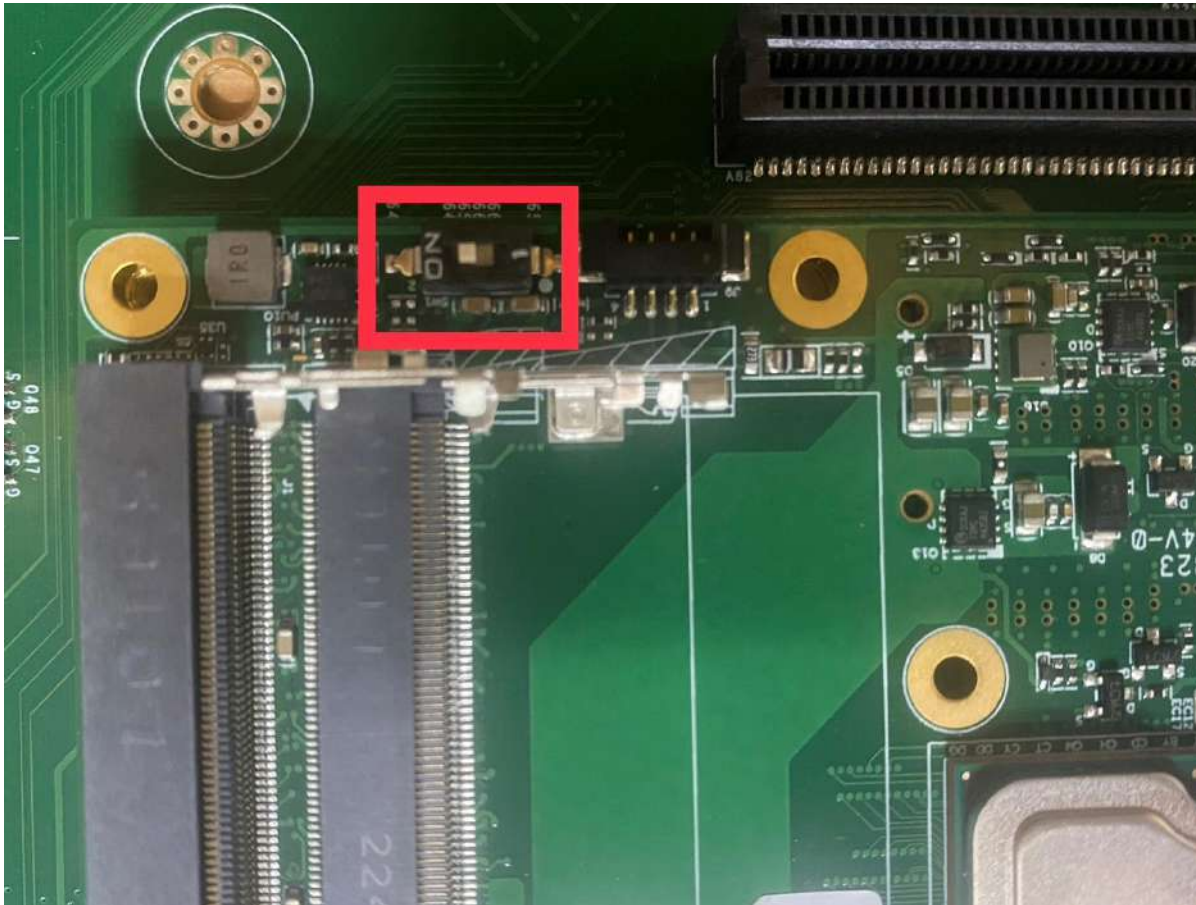
SMBUS Address	Information
0x44	SMBus ARP
0x6C	PCIe Clock Buffer
0x10 / 0x12 / 0x16 / 0x48	Reserved

Table 9 System Resources

## 8 BIOS/EC Update

PCOM-B705GT only support BIOS/EC update under UEFI shell environment, refer the following step, please.

Step 1. The switch need to set on “on”





Step 2. Unzip update file to the USB DOK (USB DOK must be FAT or FAT32 format)

Step 3. Plug the USB DOK on the target system and boot from UEFI shell



Step 4. Under the UEFI shell, change prompt to your USB DOK, the below example is “**fs0:**”

Step 5. Then change the folder with updated file and use command: “**update**” and press enter

```
EFI Shell version 2.80 [5.28]
Current running mode 1.1.2
Device mapping table
  fs0      :Removable HardDisk - Alias hd35h0b blk0
            PciRoot(0x0)/Pci(0x1E,0x0)/USB(0x7,0x0)/HD(1,MBR,0xF87AA5B3,0x20,0x3A
1FFE0)
  blk0     :Removable HardDisk - Alias hd35h0b fs0
            PciRoot(0x0)/Pci(0x1E,0x0)/USB(0x7,0x0)/HD(1,MBR,0xF87AA5B3,0x20,0x3A
1FFE0)
  blk1     :Removable BlockDevice - Alias (null)
            PciRoot(0x0)/Pci(0x1E,0x0)/USB(0x7,0x0)

Press ESC in 5 seconds to skip startup.nsh, any other key to continue.
Shell> fs0:

fs0:¥> cd PCOM-B705GT

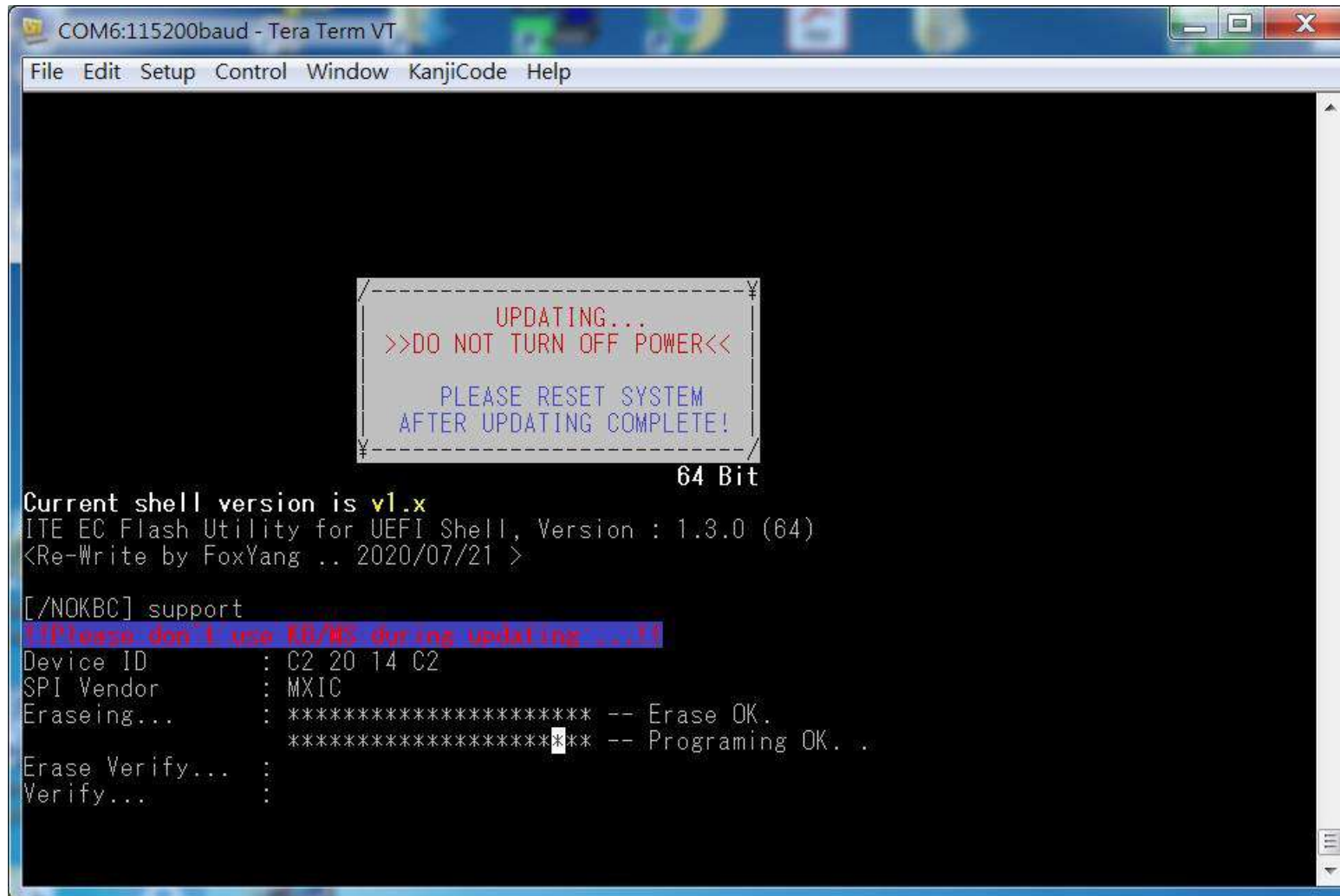
fs0:¥PCOM-B705GT> Update.efi
```

Step 6. The updating process will start and show the updating progress

Step 7. Please power off and restart the system once updating finished

```
COM6:115200baud - Tera Term VT
File Edit Setup Control Window KanjiCode Help
UPDATING...
>>DO NOT TURN OFF POWER<<
PLEASE RESET SYSTEM
AFTER UPDATING COMPLETE!
64 Bit
AMI Firmware Update Utility v5.09.02.1384
Copyright (C)2017 American Megatrends Inc. All Rights Reserved.
Reading flash ..... done
- ME Data Size checking . ok
- FFS checksums ..... ok
- Check RomLayout ..... Ok.
Erasing Boot Block ..... done
Updating Boot Block ..... done
Verifying Boot Block ..... done
Erasing Main Block ..... done
Updating Main Block ..... done
Verifying Main Block ..... done
Erasing NVRAM Block ..... done
Updating NVRAM Block ..... done
Erasing NCB Block ..... done
Updating NCB Block ..... done
Verifying NCB Block ..... done
fs0:YPCOM-B705GT>
```

(BIOS updating progress)



```
COM6:115200baud - Tera Term VT
File Edit Setup Control Window KanjiCode Help

UPDATING...
>>DO NOT TURN OFF POWER<<

PLEASE RESET SYSTEM
AFTER UPDATING COMPLETE!

64 Bit

Current shell version is v1.x
ITE EC Flash Utility for UEFI Shell, Version : 1.3.0 (64)
<Re-Write by FoxYang .. 2020/07/21 >

[/NOKBC] support
!!Please don't use KB/MS during updating...!!
Device ID      : C2 20 14 C2
SPI Vendor     : MXIC
Erasing...     : ***** -- Erase OK.
                ***** -- Programing OK. .
Erase Verify... :
Verify...     :
```

(EC updating progress)

## 9 PORTWELL Software Tool

PORTWELL Download Center

<https://www.portwell.com.tw/support-center/download-center/>

PORTWELL Evaluation Tool (PET)

The PORTWELL Evaluation Tool (PET) is an API which PORTWELL's customers can access the GPIO, I2C, SMBus, etc under Windows and Linux OS. For further information please contact PORTWELL.

## 10 Packaging Information

PCOM-B700G-NS's packaging specification will follow the Portwell standard style.

Package	Appearance	Size
Anti-Static bubble bag		180x130mm
White Paper Box		210x150x40mm
Shipping Box (10 pcs White paper box)		595x300x185mm

Table 10 Packaging information

# 11 Industry Specifications

The list below provides links to industry specifications that apply to PORTWELL modules.

Low Pin Count Interface Specification, Revision 1.0 (LPC) <http://www.intel.com/design/chipsets/industry/lpc.htm>

Universal Serial Bus (USB) Specification, Revision 2.0 <http://www.usb.org/home>

PCI Specification, Revision 2.3 <https://www.pcisig.com/specifications>

Serial ATA Specification, Revision 3.0 <http://www.serialata.org/>

PICMG® COM Express Module™ Base Specification <http://www.picmg.org/>

PCI Express Base Specification, Revision 2.0 <https://www.pcisig.com/specifications>