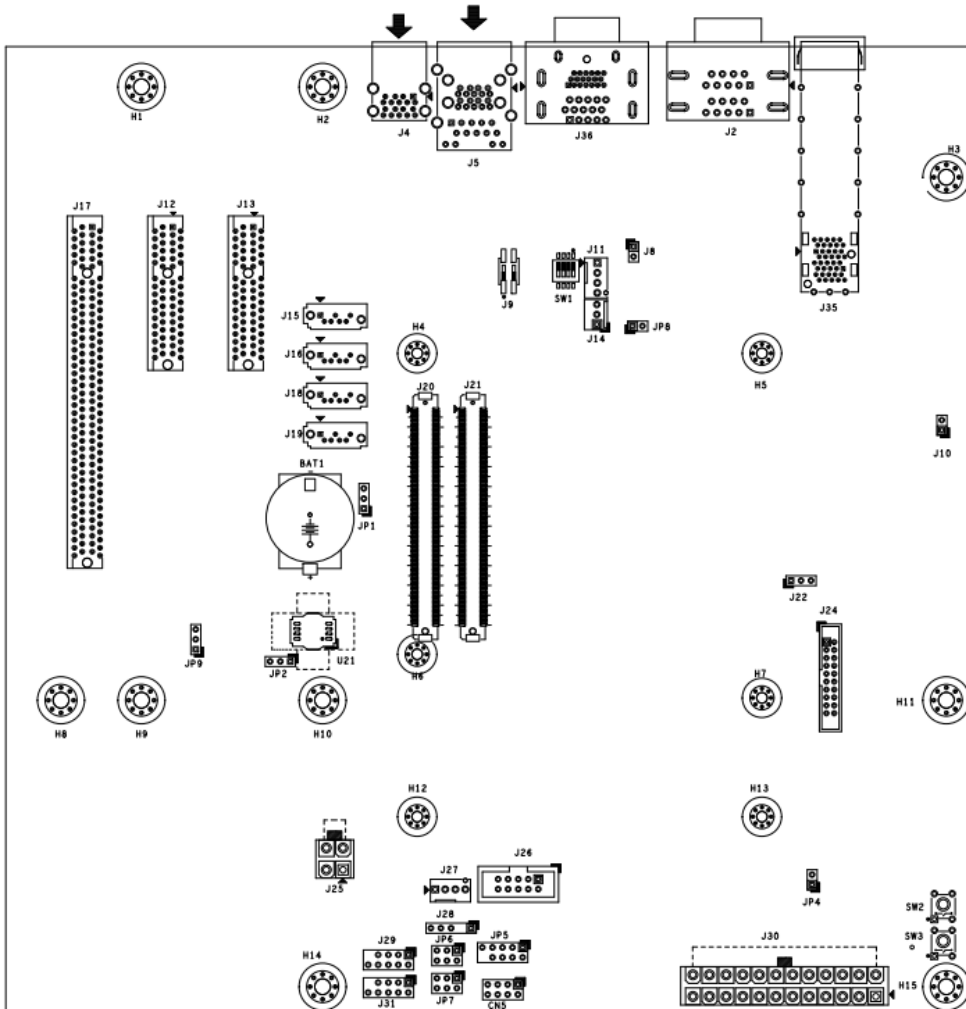


Hardware Configuration Setting

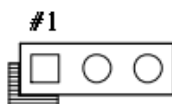
This chapter gives the definitions and shows the positions of jumpers, headers and connectors. All of the configuration jumpers on PCOM-C609 are in the proper position. The default settings shipped from factory are marked with an asterisk (★).



2-1 Jumpers

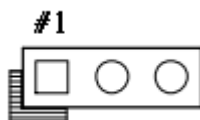
In general, jumpers on the single board computer are used to select options for certain features. Some of the jumpers are designed to be user-configurable, allowing for system enhancement. The others are for testing purpose only and should not be altered. To select any option, cover the jumper cap over (SHORT) or remove (NC) it from the jumper pins according to the following instructions. Here NC stands for “Not Connect”.

JP1:CMOS Clear



JP1	Function
1-2 Short	Normal operation★
2-3 Short	Clear CMOS contents

JP2:System Boot BIOS Selection



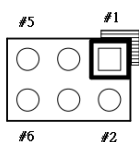
JP2	Function
2-3 Short	Boot from main board★
1-2 Short	Boot from carrier board

JP4:ATX Emulation AT Mode Selection



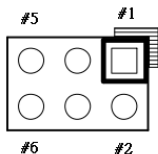
JP4	Function
1-2 Short	ATX Emulation AT Mode★
1-2 Open	ATX Mode

JP6:COM1 RI Function Selection



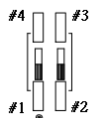
JP6	Function
1-3 Short	+5V
3-4 Short	RI Signal★
3-5 Short	+12V

JP7:COM2 RI Function Selection



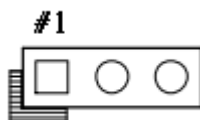
JP7	Function
1-3 Short	+5V
3-4 Short	RI Signal★
3-5 Short	+12V

J9: Carrier Board Super IO Selection



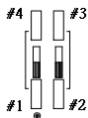
J9	Function
SW1-4(ON),SW2-3(ON)	LPC connect to SIO on carrier board
SW4-5(OFF),SW3-6(OFF)	LPC do not connect to SIO on carrier board

JP9:PCIE Clock Buffer Mode Selection



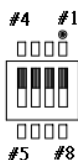
JP2	Function
2-3 Short	Select PCIE Clock Buffer BYPASS mode★
1-2 Short	Select PCIE Clock Buffer PLL mode

J10:KX4 Signal Selection



J10	Function
Short	Select Port1 KX signal
Open	Select Port0 KX signal

SW1: KX Signal Selection



SW1	Function
SW4-5(ON),SW3-6(OFF)	First & Second KX Enable signal
SW4-5(OFF),SW3-6(ON)	Third & Fourth KX Enable signal
SW2-7(ON)	Input First KX signal on SFP
SW2-7(OFF)	Input Second KX signal on SFP
SW1-8(ON)	Input Third KX signal on SFP
SW1-8(OFF)	Input Fourth KX signal on SFP

2.2 Connectors

I/O peripheral devices and Flash disk will be connected to these interface connectors

CONNECTOR	FUNCTION	REMARK
J2	COM1 & COM2 connector	
J4	USB3.0 connector	
J5	RJ45 + USB3.0 connector	
J8	CASEOPEN#	
J11	CPU fan power connector	Carrier Board
J12,J13	PCIE x4 Slot	
J17	PCIE x16 Slot	
J14	System fan power connector	Carrier Board
J15,J16,J18,J19	SATA GEN3 connector	
J20,J21	COM Express Type6 connector	
J22	I2C Header for SFP Plus EEPROM	
J24	TPM Header	
J25	ATX Power connector for module	ATX 4Pin
J26	General Purpose I/O Connector	
J27	CPU FAN power connector	Module Board
J28	SMBUS Connector	
J29	Internal USB2.0 connector	
J30	ATX Power Connector	ATX 24Pin
J31	Internal USB2.0 connector	Only one port
J36	VGA+HDMI Connector	
JP5	Front panel system connector	
CN5	UART	
SW2	Reset	

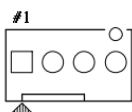
CONNECTOR	FUNCTION	REMARK
SW3	Power button	

J8 : CASEOPEN Connector



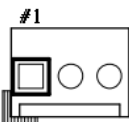
PIN No.	Signal Description
1	Signal
2	Ground

J11/J27:CPU Fan power connector



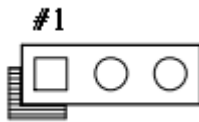
PIN No.	Signal Description
1	Ground
2	+12V
3	Fan on/off output
4	Fan Speed control

J14: System fan power connector



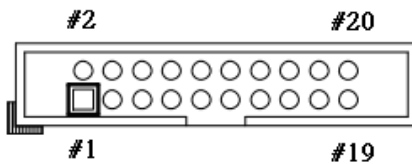
PIN No.	Signal Description
1	Ground
2	Fan speed control
3	Fan on/off output

J22: I2C Header for SFP Plus EEPROM



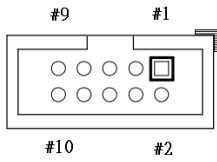
PIN No.	Signal Description
1	SFPP_EEPROM_I2C_SDA
2	Ground
3	SFPP_EEPROM_I2C_SCL

J24 : TPM Connector



PIN No.	Signal Description	PIN No.	Signal Description
1	TPM_CLK	2	GND
3	LFRAME#	4	N/C
5	TPM_RESET#	6	VCC5
7	LAD3	8	LAD2
9	VCC3	10	LAD1
11	LAD0	12	GND
13	SMB_CLK	14	SMB_DATA
15	3VDUAL	16	SERIRQ
17	GND	18	N/C
19	N/C	20	N/C

J26 : General Purpose I/O Connector



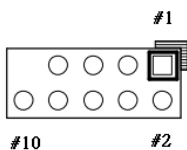
PIN No.	Signal Description	PIN No.	Signal Description
1	GPIO	2	GPIO
3	GPIO	4	GPIO
5	GPIO	6	GPIO
7	GPIO	8	GPIO
9	Ground	10	+5V

J28:SMBUS Connector



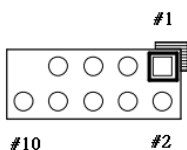
PIN No.	Signal Description
1	SMB_CLK
2	Key(no pin)
3	Ground
4	SMB_DAT
5	+3VDUAL

J29 : Internal USB Connector



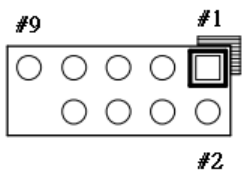
PIN No.	Signal Description	PIN No.	Signal Description
1	+5V	2	+5V
3	USB-	4	USB-
5	USB+	6	USB+
7	Ground	8	Ground
	Key(no pin)	10	N/C

J31 : Internal USB Connector



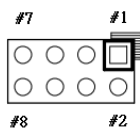
PIN No.	Signal Description	PIN No.	Signal Description
1	+5V	2	RSVD
3	USB-	4	RSVD
5	USB+	6	RSVD
7	Ground	8	RSVD
	Key(no pin)	10	RSVD

JP5 : Front panel system connector



PIN No.	Signal Description	PIN No.	Signal Description
1	SATA_LED(+)	2	SUSLED(+)
3	SATA_LED(-)	4	SUSLED(-)
5	Power Button (-)	6	Power Button(+)
7	Reset(+)	8	Reset(-)
9	N/C	10	Key(no pin)

CN5 : UART



PIN No.	Signal Description	PIN No.	Signal Description
1	SER0_TX	2	SER1_TX
3	SER0_RX	4	SER1_RX
5	Ground	6	Ground
7	SLEEP#	8	LID#